ACADEMIC YEAR 2025-26						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
1	Briefing on "**SEMICON CONCLAVE-2K25**" held at INTEL Technology, Bangalore	12.08.2025	THIYANAND M 5th Sem coordinated by Prof. Rajani B Rai Dept. of ECE,VCET PUTTUR	5th	63	PO1 PO2 PO9 PO11 PSO2





The Department of Electronics and Communication Engineering at **Vivekananda College of Engineering & Technology, Puttur**, organized a **briefing session on "SEMICON CONCLAVE–2K25"** on **12th August 2025**. The session was delivered by **Mr. Thiyanand M**, a **5th Semester student**, and was coordinated by **Prof. Rajani B Rai**, Department of ECE, VCET, Puttur.

During the briefing, Mr. Thiyanand M shared insights gained from the SEMICON Conclave–2K25, highlighting recent advancements in semiconductor technology, industry trends, and career opportunities in the semiconductor domain. The session helped students understand the importance of semiconductor manufacturing, research, and innovation in today's technology-driven world.

The session was attended by **63 students of the 5th Semester, ECE**, and proved to be informative and engaging. It enhanced students' awareness of the semiconductor industry and motivated them to explore future opportunities in this rapidly growing field.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
2	Awareness program on **"UPSC-IAS, KPSC & All competitive Exams".**	01.09.2025	Mr. Prashanth M.V MVP Vishwakosha	2nd and 3rd year EC	120	PO7 PO8 PO9 PO11



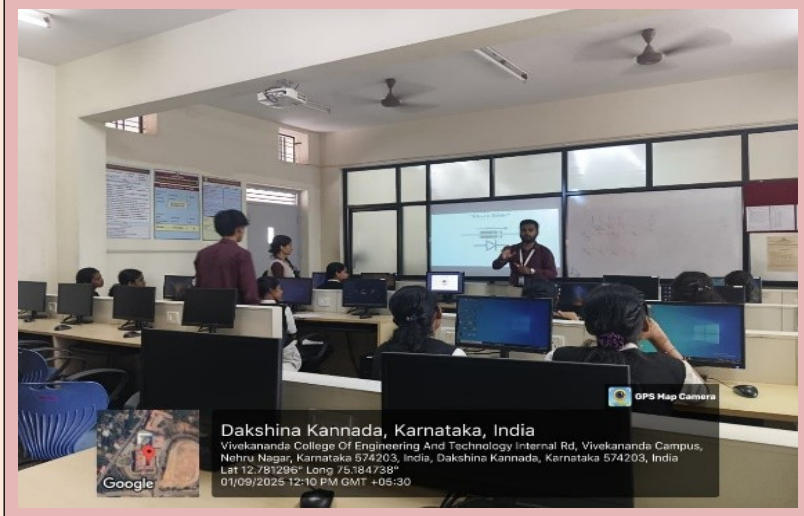


The Department of Electronics and Communication Engineering at **Vivekananda College of Engineering & Technology, Puttur**, organized an **awareness program on "UPSC-IAS, KPSC & All Competitive Examinations"** on **1st September 2025**. The session was conducted by **Mr. Prashanth M.V**, representing **MVP Vishwakosha**.

During the program, Mr. Prashanth M.V provided detailed information about various competitive examinations, including UPSC-IAS and KPSC. He explained the examination patterns, eligibility criteria, stages of selection, and effective preparation strategies. The speaker also emphasized time management, consistency, and the importance of a structured study plan for achieving success in competitive examinations.

The program was attended by 120 **students of 2nd and 3rd year Electronics and Communication Engineering**.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
3	A hands on session on " **Orcad PSpice software**"	01.09.2025	Session was conducted by the 5th sem ECE students and coordinated by Prof. Shreyas H. Dept. of ECE,VCET PUTTUR.	Narendra PU College, Puttur.	85	PO1 PO2 PO5 PO9 PO11 PSO1





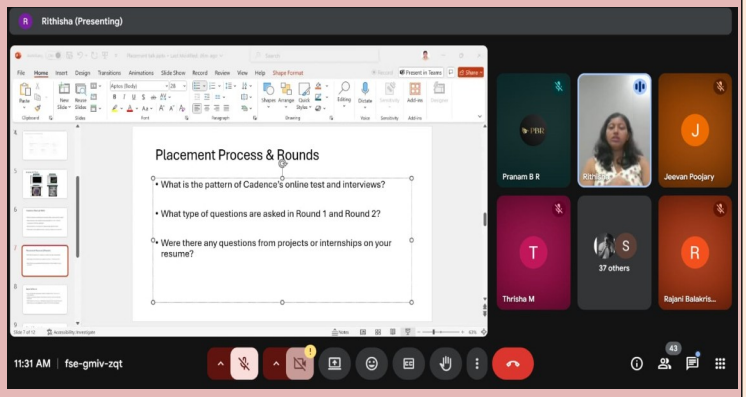
The Department of Electronics and Communication Engineering at **Vivekananda College of Engineering & Technology, Puttur**, organized a **hands-on session on "OrCAD PSpice Software"** on **1st September 2025** at **Narendra PU College, Puttur**. The session was conducted by **5th Semester ECE students** and coordinated by **Prof. Shreyas H.**, Department of ECE, VCET, Puttur.

The objective of the session was to provide practical exposure to OrCAD PSpice software for circuit design and simulation. The student instructors demonstrated the basic features of the software, including schematic capture, simulation techniques, and analysis of electronic circuits. Participants were guided through real-time simulations, enabling them to gain hands-on experience with industry-relevant tools.

The session was attended by **85 students** of Narendra PU College and was highly interactive and beneficial. It enhanced the participants' understanding of electronic circuit simulation and encouraged interest in electronics and engineering applications.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
4	A session on " **placement guidance** "	06.09.2025	Ms. Rithisha Shetty Design Engineer Cadence	Final Year Students	60	PO7 PO8 PO9 PO11





The Department of Electronics and Communication Engineering at **Vivekananda College of Engineering & Technology, Puttur**, organized a **session on "Placement Guidance"** on **6th September 2025**. The session was conducted by **Ms. Rithisha Shetty**, **Design Engineer at Cadence**.

During the session, Ms. Rithisha Shetty provided valuable guidance on placement preparation, including industry expectations, essential technical skills, and the importance of aptitude and communication skills. She also shared insights into the recruitment process, interview strategies, and ways to build a strong professional profile to enhance employability.

The session was attended by **60 final-year students** and proved to be highly informative and motivating. It helped students gain clarity on placement preparation and encouraged them to approach the recruitment process with greater confidence.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
5	A Workshop on **"NETWORKING WITH WIRESHARK".**	24.10.2025 to 25.10.2025	Mr. Gautham Shankar. N R&D Software Engineer Telit Cinterion	3rd EC and 5th EC	50	PO1 PO2 PO3 PO4 PO5 PO11 PSO2





The Department of Electronics and Communication Engineering at **Vivekananda College of Engineering & Technology, Puttur**, successfully conducted a **two-day hands-on workshop titled "Networking with Wireshark"** from **24th to 25th October 2025**. The program was led by **Mr. Gautham Shankar N, R&D Software Engineer at Telit Cinterion, Bangalore.**

The workshop emphasized practical learning through live demonstrations of packet capturing and network traffic analysis using the Wireshark tool. Participants were introduced to various network protocols and were trained to analyze, interpret, and troubleshoot network-related issues effectively. The interactive sessions encouraged active participation and improved the students' practical understanding of networking concepts.

A total of 50 **students from the 3rd and 5th Semester of the Electronics and Communication Engineering department** took part in the workshop. The program provided valuable industry exposure and significantly enhanced the technical competence of the participants in the field of computer networking.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
6	A session on "**Classroom to Corporate**"	25.10.2025	Mr. Gautham Shankar. N R&D Software Engineer Telit Cinterion Bengaluru.	5th Sem	50	PO1 PO2 PO9 PO11





The Department of Electronics and Communication Engineering at **Vivekananda College of Engineering & Technology, Puttur**, organized a **session titled "Classroom to Corporate"** on **25th October 2025**. The session was delivered by **Mr. Gautham Shankar N, R&D Software Engineer at Telit Cinterion, Bengaluru**.

The session focused on bridging the gap between academic learning and industry expectations. Mr. Gautham Shankar N shared his professional experiences and provided insights into workplace culture, essential technical competencies, and soft skills required to succeed in the corporate environment. He also guided students on career planning, continuous learning, and adapting to professional challenges.

The session was attended by 50 **students of the 5th Semester Electronics and Communication Engineering**. The interaction was informative and inspiring, helping students gain a clearer perspective on transitioning from classroom learning to corporate life.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
7	A Event on "**Circuitrix-2025**"	28.10.2025	Coordinated by Prof. Akshay S. P	3rd EC and 5th EC	120	PO1,PO2 PO3, PO4 PO5, PO6 PO8, PO9 PO10,PO11 PSO1





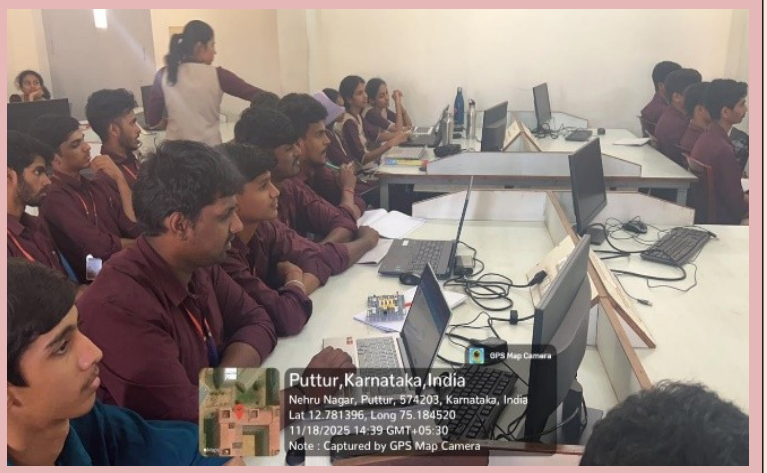
The Department of Electronics and Communication Engineering at **Vivekananda College of Engineering & Technology, Puttur**, organized an event titled **"Circuitrix–2025"** on **28th October 2025**. The event was coordinated by **Prof. Akshay S. P**, Department of ECE, VCET, Puttur.

The technical activities and event structure were explained by **Mr. Thiyanand M**, who briefed the participants on the objectives, rules, and execution of the event. The program encouraged analytical thinking, teamwork, and practical application of electronic circuit concepts among students.

The event witnessed active participation from **120 students of 3rd and 5th Semester Electronics and Communication Engineering**. The event was successfully conducted and provided an engaging platform for students to enhance their technical skills and collaborative learning.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
8	A workshop on "**IoT Using Arduino and Its Applications**"	18.11.2025	Session was conducted by the 5th sem EC student volunteers and coordinated by Prof. Mahabaleshwar Bhat P Prof. Nisha G. R and Prof. Shreyas H. Dept. of ECE,VCET PUTTUR.	2nd year EC	60	PO1,PO2 PO3,PO4 PO5,PO6 PO8,PO10 PO11 PSO1



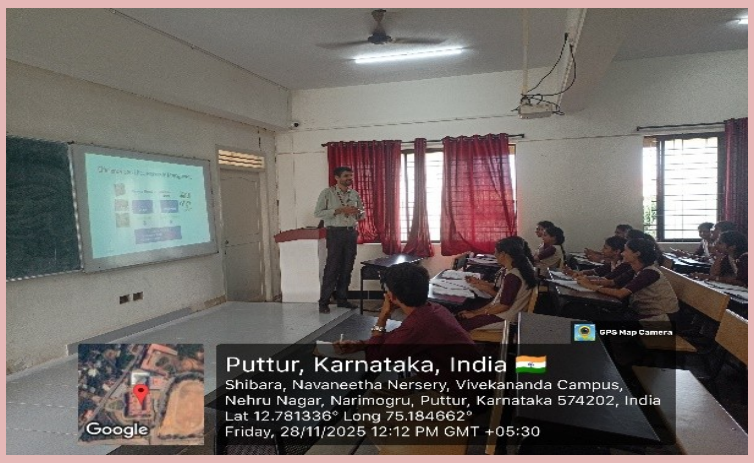


The Department of Electronics and Communication Engineering at **Vivekananda College of Engineering & Technology, Puttur**, organized a **workshop on "IoT Using Arduino and Its Applications"** on **18th November 2025**. The session was conducted by **5th Semester Electronics and Communication Engineering student volunteers** and was coordinated by **Prof. Mahabaleshwar Bhat P, Prof. Nisha G. R, and Prof. Shreyas H.**, Department of ECE, VCET, Puttur.

The workshop focused on introducing Internet of Things (IoT) concepts using Arduino platforms. Participants were guided through hands-on demonstrations involving sensor interfacing, data acquisition, and real-time applications of IoT systems. The session helped students understand practical implementation aspects and the relevance of IoT in modern engineering applications.

The workshop was attended by **60 second-year Electronics and Communication Engineering students**. The program was interactive and effective, enhancing students' technical knowledge and encouraging experiential learning in the area of IoT and embedded systems.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
9	A session on **"Entrepreneurship Opportunities in Diverse Fields in India."**	28.11.2025	Prof. Shreyas H. Dept. of ECE,VCET PUTTUR.	5thyear EC	60	PO1,PO2 PO3,PO5 PO6,PO7 PO8,PO11





A session on **Entrepreneurship Opportunities in Diverse Fields in India** was organized on 28th November 2025 for the 5th semester Electronics and Communication Engineering students as part of the TIME (BEC501) course. The session was delivered by Prof. Shreyas H, Assistant Professor, Department of ECE, VCET, Puttur. He addressed the students on the wide range of fields that BE graduates can explore, highlighting emerging sectors, technological domains, and industry demands. Mr. Shreyas also explained the various career pathways available for students who wish to become entrepreneurs and discussed the skills required to start and sustain a successful venture. He emphasized the importance of innovation, problem-solving, and understanding market needs in today's competitive environment. The session was highly interactive, and students actively engaged by clarifying their ideas and doubts related to entrepreneurship, business models, and funding possibilities. The talk helped students gain awareness about opportunities beyond traditional employment and encouraged them to consider entrepreneurship as a viable career option. Overall, the session was informative and beneficial in motivating students to think creatively about their future career paths.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
10	A Session on **"From resume to recruitment: Making your first impression count".**	30.10.2025	**Ms. Smitha** Assistant Professor Dept. Of Civil Engg. **Ms. Sheethal S K** Placement Associate Dept.Of MBA	3rd year EC	60	PO7 PO8 PO9 PO11



The Department of Electronics and Communication Engineering at **Vivekananda College of Engineering & Technology, Puttur**, conducted a **session titled "From Resume to Recruitment: Making Your First Impression Count"** on **30th October 2025**. The session was handled by **Ms. Smitha**, Assistant Professor, Department of Civil Engineering, and **Ms. Sheethal S. K**, Placement Associate, Department of MBA.

The session emphasized the importance of creating an effective resume and presenting oneself confidently during the recruitment process. The speakers provided practical guidance on resume structuring, interview etiquette, and professional communication skills, helping students understand how first impressions influence selection outcomes.

The session was attended by **60 third-year Electronics and Communication Engineering students**. The program was informative and beneficial, equipping students with essential skills required for successful placement and career readiness.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
11	A session on **"Digital Safety and Cyber Fraud Awareness Program"**	01.12.2025	Prof. Shreyas H, Assistant Professor, Department of ECE, VCET, Puttur.	7th SEM EC	16	PO1,PO2 PO5,PO6 PO7,PO11 PSO2





A session on **Digital Safety and Cyber Fraud Awareness Program** was conducted on 1st December 2025 for the 7th semester Electronics and Communication Engineering students as part of the Computer and Network Security (BEC714B) course. The session was delivered by Prof. Shreyas H, Assistant Professor, Department of ECE, VCET, Puttur.

He highlighted the growing importance of cyber security in daily life and explained various types of online threats such as phishing, financial frauds, OTP scams, identity theft, and social engineering techniques. Prof. Shreyas also emphasized the need to educate rural and elderly people about safe digital practices, as they are more prone to falling victim to cyber fraud. He discussed simple and practical methods to create awareness, including recognizing fake messages or calls, safeguarding personal information, and reporting suspicious activities. The session was very interactive, with students actively engaging and clarifying their doubts regarding cyber safety and real-life fraud scenarios. It helped students understand both the technical and social aspects of cyber security and motivated them to promote safe digital habits in their communities. Overall, the session was informative and strengthened students' understanding of responsible online behaviour.

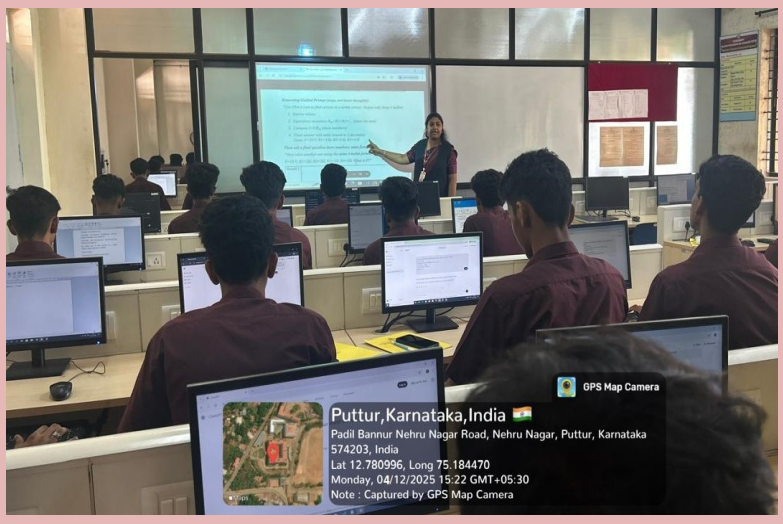
ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
12	A session on **"Awareness and Technology Exploration Activity on Modern Wireless Communication Systems"**	02.12.2025	Prof. Akshay S P, Assistant Professor, Department of ECE, VCET, Puttur.	7th SEM EC	60	PO1,PO2 PO3,PO4 PO5,PO11 PSO2



A session on "Awareness and Technology Exploration Activity on Modern Wireless Communication Systems" was organized on 02/12/25 for 7th sem EC students as apart of Wireless Communication Systems course. The session was delivered by Prof. Akshay S P, Assistant Professor, Department of EC.

The activity aims to relate theoretical concepts to real-world applications, enhance technical awareness, and develop interest in emerging communication technologies. The focus was on understanding how these technologies are applied in real-life mobile communication systems, internet services, and wireless networks.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
13	A workshop on **"Simulation of Digital Electronic Circuits using PSpice"**	04.12.2025	Prof. Nisha G R, Assistant Professor, Department of ECE, VCET, Puttur.	3rd SEM EC	66	PO1 PO2 PO5 PO11 PS01



The Department of Electronics and Communication Engineering at Vivekananda College of Engineering & Technology, Puttur, organized a workshop on "Simulation of Digital Electronic Circuits using PSpice" on December 4, 2025. The session was conducted by Mrs. Nisha G R.

The workshop focused on providing students with valuable insights into how electronic circuits behave under various conditions. By using simulation tools, students were able to analyze and understand their circuit designs better. This hands-on experience helped them connect theoretical knowledge with real-world applications, enhancing their understanding of digital circuit performance and functionality.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
14	An Activity on "**Introduction to Prompt Engineering**"	08.12.2025	Prof. Nisha G R & Prof. Nirupama K Assistant Professor, Department of ECE, VCET, Puttur.	1st SEM EC	58	PO1 PO2 PO5 PO11 PSO1





The Department of Electronics and Communication Engineering at Vivekananda College of Engineering & Technology, Puttur, organized an Activity on "Introduction to Prompt Engineering" on December 8, 2025. The session was conducted by Mrs. Nisha G R. and Mrs. Nirupama.

The Activity focused on providing students with valuable insights into how to write effective prompts using different tools like chatgpt, gemini, Microsoft Copilot and Perplexity. By using such tools, students were able to analyze and understand their prompts better. This session experience helped them to connect theoretical knowledge with real-world applications, enhancing their understanding of prompt engineering.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
15	A talk on "**Towards Embedded AI and TinyML**"	13.12.2025	Dr. Ashok Rao Former Head, Network Project, CEDT, IISc, Bangalore	3rd SEM EC	60	PO1,PO2 PO3,PO4 PO5,PO6 PO11 PSO2



The Department of Electronics and Communication Engineering at **Vivekananda College of Engineering & Technology, Puttur**, organized a **technical talk on "Towards Embedded AI and TinyML"** on **13th December 2025**. The talk was delivered by **Dr. Ashok Rao**, Former Head, Network Project, **CEDT, IISc, Bangalore**.

During the session, Dr. Ashok Rao discussed the fundamentals of Embedded Artificial Intelligence and TinyML, highlighting their significance in resource-constrained systems. He shared insights into real-world applications, emerging trends, and future prospects of AI-driven embedded solutions, offering students a deeper understanding of this evolving technological domain.

The talk was attended by **60 students of the 3rd Semester Electronics and Communication Engineering**. The session was highly informative and inspired students to explore advanced concepts in embedded systems and artificial intelligence.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
16	A session on **"2D and 3D radiation pattern analysis of various antennas using MATLAB simulation"**.	15.12.2025	Prof. Nithin Assistant Professor, Department of ECE, VCET, Puttur.	7th SEM EC	66	PO1 PO2 PO5 PO11 PSO2





A session on ***2D and 3D radiation pattern analysis of various antennas using MATLAB simulation*** was conducted on 15th December 2025 for the 7th semester Electronics and Communication Engineering students as part of the Microwave Engineering and Antenna Theory (BEC701) course. The session was delivered by Prof. Nithin, Assistant Professor, Department of ECE, VCET, Puttur.

The activity enabled students to visualize antenna radiation characteristics and understand key performance parameters through simulation-based learning. It enhanced their conceptual clarity and practical exposure to antenna analysis, thereby strengthening their knowledge in antenna and wave propagation concepts. The talk was attended by **66 students of the 7th Semester Electronics and Communication Engineering**.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
17	A Workshop on **"Introduction to HTML, CSS and JavaScript using CodePen".**	17.12.2025	**Prof.Shivaprasad** Assistant Professor Dept. of ECE,VCET PUTTUR.	1st year EC	58	PO1 PO2 PO3 PO5 PO11



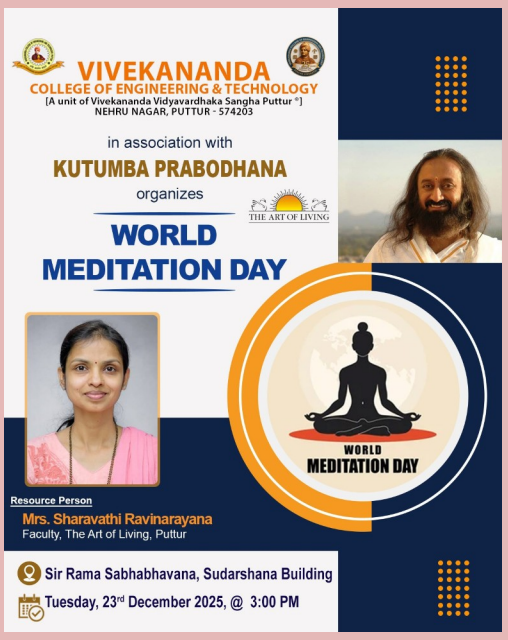


The Department of Electronics and Communication Engineering at **Vivekananda College of Engineering & Technology, Puttur**, organized a **workshop on "Introduction to HTML, CSS and JavaScript using CodePen"** on **17th December 2025**. The workshop was conducted by **Prof. Shivaprasad**, Assistant Professor, Department of ECE, VCET, Puttur, and was coordinated by **Prof. Nithin, Assistant Professor, Department of ECE, VCET, Puttur.**

The session introduced students to the fundamentals of web development, focusing on HTML for structure, CSS for styling, and JavaScript for interactivity using the CodePen platform. Live demonstrations and hands-on activities enabled participants to practice coding in real time and understand the basics of front-end development.

The workshop was attended by **58 first-year Electronics and Communication Engineering students**. The program was engaging and effective, helping students build a strong foundation in web technologies and encouraging further exploration in web development.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
18	A talk on **"Word Meditation Day".**	23.12.2025	**Mrs.Sharavathi Ravinarayana** Faculty, Art of Living, PUTTUR.	3rd SEM EC	60	PO6 PO7 PO11





The Department of Electronics and Communication Engineering at **Vivekananda College of Engineering & Technology, Puttur**, organized a **talk on "World Meditation Day"** on **23rd December 2025**. The session was delivered by **Mrs. Sharavathi Ravinarayana**, Faculty, **Art of Living, Puttur and was organized by Prof. Prabha G S, Assistant Professor, Department of ECE, VCET, Puttur.**

The talk highlighted the importance of meditation in managing stress, improving concentration, and maintaining mental well-being, especially for students. Mrs. Sharavathi Ravinarayana explained simple meditation techniques and emphasized the role of mindfulness in enhancing academic performance and overall personal development.

The talk was attended by **60 students of the 3rd Semester Electronics and Communication Engineering**. The session was calm, engaging, and beneficial, encouraging students to incorporate meditation practices into their daily routine for better focus and balance.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
19	A workshop on **" Simulation of Different Electronics Circuits using Lakshya software "**	26.12.2025	Mrs. Prabha G S Assistant Professor Dept. of ECE,VCET PUTTUR.	1st SEM CD	58	PO1 PO2 PO5 PSO1





The Department of Electronics and Communication Engineering at **Vivekananda College of Engineering & Technology, Puttur**, conducted a **workshop on "Simulation of Different Electronic Circuits Using Lakshya Software"** on **26th December 2025**. The workshop was handled by **Mrs. Prabha G. S.**, Assistant Professor, Department of ECE, VCET, Puttur.

The session focused on familiarizing students with the Lakshya simulation software for analyzing basic electronic circuits. Through demonstrations and guided practice, participants learned how to simulate and observe the behavior of various electronic components and circuits, thereby strengthening their conceptual understanding.

The workshop was attended by **58 first-semester CD students**. The program was informative and beneficial, helping students gain practical exposure to electronic circuit simulation tools at an early stage of their academic journey.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
20	A simulation-based activity on "***Power Electronic Devices and Applications.***"	29.12.2025	**Prof.** Mahabaleshwara Bhat P Assistant Professor Dept. of ECE,VCET PUTTUR.	3rd SEM EC	66	PO1 PO2 PO5 PO11 PS01





A simulation-based activity on *Power Electronic Devices and Applications* was conducted on 29/12/2025 using OrCAD PSpice as part of the course Electronic Principles and Circuits for III Semester EC students, with the participation of 66 students. The workshop was handled by Prof. Mahabaleshwara Bhat P., Assistant Professor, Department of ECE, VCET, Puttur.

The activity focused on the simulation and analysis of SCR and Power MOSFET based circuits to understand their operating characteristics, triggering behavior, and switching performance. Students simulated controlled rectifier circuits using SCRs and power switching circuits using MOSFETs, observed voltage and current waveforms, and analyzed the effect of control parameters on circuit operation. The activity effectively bridged theoretical concepts with simulation practice and enhanced students' analytical and application-oriented learning.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
21	A simulation-based activity on "**Transient Analysis using Psipce.**"	30.12.2025	Prof. Sowmya Anil & Prof. Rajani Rai B Assistant Professor, Department of ECE, VCET, Puttur.	3rd SEM EC	66	PO1 PO2 PO5 PO11 PS01





The Department of Electronics and Communication Engineering, Vivekananda College of Engineering & Technology, Puttur, organised a Workshop on "Simulation and analysis of circuits using Psipce" on 30th December 2025. Mrs Sowmya Anil and Mrs Rajani Rai B conducted the session.

The workshop provided specific insights into how the circuit behaves under initial conditions. Simulation tools offered a detailed analysis of circuits' transient performance, enabling students to identify and understand their designs and the behaviour of the circuit elements. They allowed students to apply theoretical knowledge to practical scenarios, helping them understand how circuits work in real-world situations.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
22	A hands-on session on **"Analog and Digital simulation using LAKSHYA simulation software."**	05.01.2026	Prof. Rajani B Rai Dept. of ECE,VCET PUTTUR	1st Sem	60	PO1,PO2 PO3,PO4 PO5, PO11 PSO1





A hands-on, simulation-based learning activity was conducted for first-year engineering students of Artificial Intelligence and Machine Learning on January 5, 2025, using the LAKSHYA simulation software.

The session aimed to provide practical exposure to fundamental analog and digital electronic circuits through interactive simulation. The activity covered the working and applications of a rectifier circuit and operational amplifier circuits, followed by the simulation and implementation of digital circuits, including a half adder and the realization of a Boolean logic expression. The session helped students correlate theoretical concepts with practical implementation, improved logical thinking, and enhanced their understanding of electronic circuit behavior.

Ms. Rajani Rai B was the resource person. A total of 60 students attended the session.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
23	A session on **"Real-Time Analysis of Computer Organization and Architecture in Modern Computing Systems"**	05.01.2026	Prof. Akshay S P Dept. of ECE,VCET PUTTUR	3rd SEM EC	66	PO1,PO2 PO3,PO4 PO5,PO11 PS01



A session on "Real-Time Analysis of Computer Organization and Architecture in Modern Computing Systems" was organized on 05/01/26 for 3rd sem EC students as apart of Computer Organization and Architecture course. The session was delivered by Prof. Akshay S P, Assistant Professor, Department of EC.

The session aims to understand how concepts of Computer Organization and Architecture are applied in real-time systems. The session focuses on CPU organization, memory hierarchy, pipelining, and I/O mechanisms used in real-time computing environments. Real-time systems were studied to analyze how processor architecture and memory organization affect system performance and response time. Real-world examples such as microcontrollers, real-time operating systems (RTOS), and embedded processors were analyzed.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
24	**A workshop on "High Speed PCB Design."**	06.03.2026	Mr. Surendra M. ECAD Comm Manager-Altium	6th SEM EC	63	PO1 PO2 PO5 PO11 PS02





A workshop on **"High Speed PCB Design"** was organized on **06/03/2026** for **6th semester EC students** as a part of the academic curriculum to enhance practical knowledge in modern electronic design. The session was delivered by **Mr. Surendra M., ECAD Comm Manager – Altium**.

The workshop aimed to provide an understanding of high-speed PCB design concepts used in modern electronic systems. The session focused on important aspects such as PCB layout techniques, signal integrity, routing strategies, layer stack-up design, and challenges involved in high-frequency circuit design. The importance of proper component placement, grounding techniques, and noise reduction methods was also discussed.

Real-time design considerations and industry practices were explained to help students understand how professional PCB design tools are used in the electronics industry. Around **63 students** actively participated in the workshop and gained valuable knowledge about high-speed PCB design methodologies and industry standards.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
25	A activity on " CMOS schematic and layout design using Microwind and DSCH2 software."	15.04.2026	Ms.Rajani Rai B and Ms. Sowmya Anil Department of EC	6th Sem	64	PO1 PO2 PO5 PO11 PSO1





A workshop on **"CMOS Schematic and Layout Design Using Microwind and DSCH2 Software"** was organized on **15.04.2026** for **6th semester Electronics and Communication Engineering students** as a part of the academic curriculum to strengthen their practical understanding of VLSI design concepts.

The session was conducted by **Ms. Rajani Rai B.** and **Ms. Sowmya Anil** from the Department of Electronics and Communication Engineering. The workshop focused on the fundamentals of CMOS circuit design, schematic creation, transistor-level implementation, and layout design using **Microwind** and **DSCH2** software tools. Students were introduced to the complete design flow, including logic circuit design, simulation, layout generation, and verification, enabling them to gain hands-on experience with industry-relevant VLSI design software.

The resource persons also highlighted the significance of CMOS technology in modern integrated circuit design and semiconductor applications. Around **64 students** actively participated in the activity and enhanced their practical knowledge of CMOS schematic and layout design methodologies.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
26	A workshop on "Advanced VLSI Design and Digital Systems."	21.04.2026	Thiyanand M 6th Sem (IEEE Communication Society and Association of Department of EC)	6th Sem & 4th Sem	27	PO1 PO2 PO5 PO11 PSO1





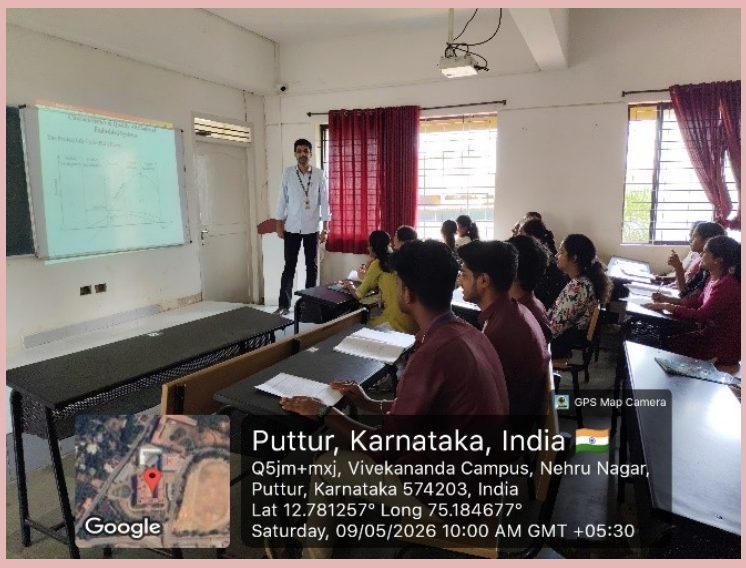
A workshop on **"Advanced VLSI Design and Digital Systems"** was organized on **21.04.2026** for **4th and 6th semester Electronics and Communication Engineering students** to enhance their knowledge of contemporary VLSI design techniques and digital system implementation.

The session was delivered by **Mr. Thiyanand M.** and was jointly organized by the **IEEE Communication Society** and the **Association of the Department of Electronics and Communication Engineering**. The workshop covered advanced concepts in VLSI design, digital circuit implementation, design methodologies, and emerging trends in semiconductor technology. Participants were introduced to practical design approaches, industry practices, and the significance of VLSI in developing efficient and high-performance digital systems.

The interactive session provided valuable insights into current technologies and career opportunities in the VLSI domain. Around **27 students** from the **4th and 6th semesters** actively participated in the workshop and gained a deeper understanding of advanced VLSI design concepts and digital system applications.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
27	A session on "Embedded Systems in Automotive Applications."	09.05.2026	Mr. Shreyas H Department of EC	6th Sem	64	PO1 PO2 PO11 PSO1





A session on **"Embedded Systems in Automotive Applications"** was organized on **09.05.2026** for **6th semester Electronics and Communication Engineering students** to enhance their understanding of embedded technologies used in the automotive industry.

The session was conducted by **Mr. Shreyas H.** from the **Department of Electronics and Communication Engineering**. He discussed the fundamentals of embedded systems, their applications in automotive electronics, electronic control units (ECUs), sensors, and real-time control systems. The session also emphasized the growing importance of embedded technology in electric vehicles, vehicle automation, and intelligent transportation systems.

Around **64 students** actively participated in the session and gained valuable insights into the applications and industry relevance of embedded systems in modern automobiles.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
28	A session on "Design and Analysis of PID Controller for Dynamic System Stability Using Simulink."	20.05.2026	Mrs Sowmya Anil and Mr Shivaprasad Department of EC	4th Sem	66	PO1 PO2 PO5 PO11 PSO1





A session on **"Design and Analysis of PID Controller for Dynamic System Stability Using Simulink"** was conducted on **20.05.2026** for **4th semester Electronics and Communication Engineering students** with the objective of enhancing their understanding of control system analysis and simulation.

The session was led by **Mrs. Sowmya Anil** and **Mr. Shivaprasad** from the **Department of Electronics and Communication Engineering**. During the session, the resource persons explained the principles of PID controllers, their significance in achieving dynamic system stability, and the application of **MATLAB Simulink** for modeling and analyzing control systems. Students also gained exposure to PID controller tuning techniques and the evaluation of system performance through simulation.

The session enabled participants to relate theoretical concepts with practical implementation. A total of **66 students** actively participated and benefited from the hands-on learning experience.