ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
1	A talk on "Beyond Graduation..."	26.08.2024	Rear Admiral C. R. Praveen Nair Indian Navy	7th	64	PO8 PO9 PO10 PO11 PO12







The Indian Society for Technical Education (ISTE), in collaboration with the Institution's Innovation Council (IIC), organized an insightful talk titled "Beyond Graduation..." delivered by Rear Admiral C.R. Praveen Nair of the Indian Navy. He shared valuable information on the diverse career opportunities within the Indian Navy, the exams students need to pursue these paths, and provided guidance on applying for scholarships and managing finances for those aiming to study at prestigious foreign universities.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
2	A technical talk on preparing for **SAP drive.**	06-09-2024	**Ms. Prathiksha K S** Alumna, ECE Batch 2018 SAP Labs	7th	64	**PO1** **PO2** **PO7** **PO8** **PO12** **PSO2**





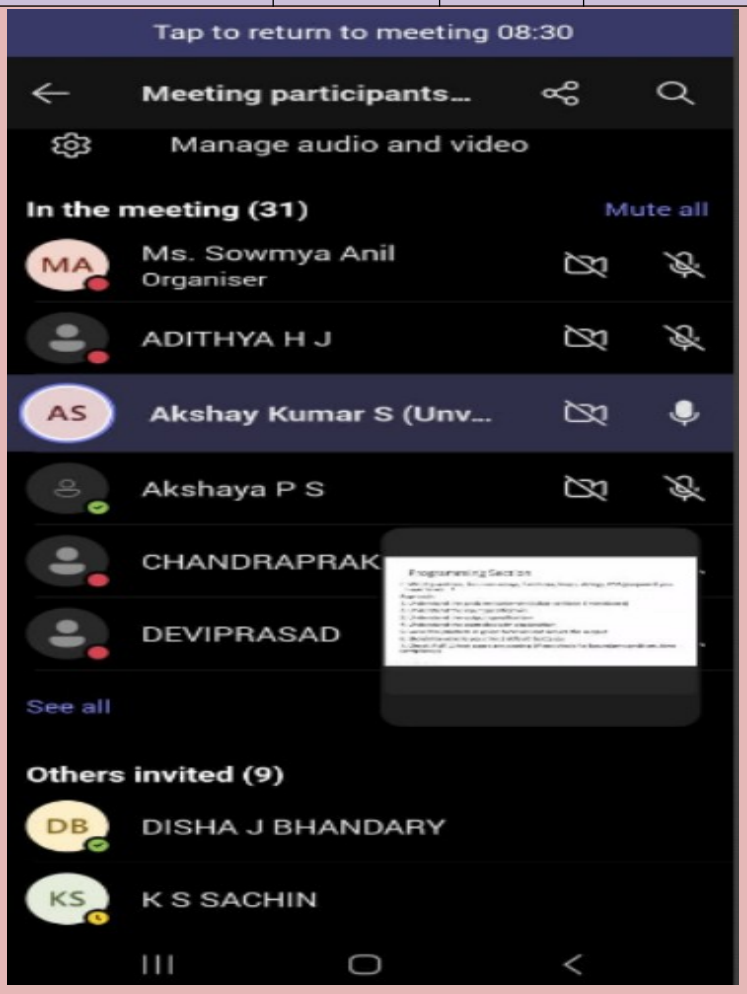
The Department of Electronics and Communication Engineering at **Vivekananda College of Engineering & Technology, Puttur**, organized a **technical talk** on **"Preparing for the SAP Drive"** on **6th September 2024**. The session was conducted by **Ms. Prathiksha**, a representative from the **SAP Lab in Bangalore**, who provided valuable insights into the **SAP recruitment process**.

During the session, Ms. Prathiksha elaborated on the various stages of the recruitment process, the skills and knowledge required to succeed in the drive, and the expectations SAP has from potential candidates. She also shared tips and strategies on how to effectively prepare for the selection process, which includes **technical assessments**, **interviews**, and **aptitude tests**.

The session was specifically targeted at **final-year students of the Electronics and Communication Engineering** department, offering them an excellent opportunity to understand what SAP looks for in applicants and how they can stand out in the highly competitive recruitment process. The talk proved to be both insightful and motivating for the students, helping them feel more confident as they approach the recruitment drive.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
3	A technical talk on preparing for **SASKEN Placement drive.**	10-10-2024	**Mr. Akshay Kumar** Software Engineer Kernel & Driver Linux Sasken, Bangalore	7th	64	**PO1 PO2 PO7 PO8 PO12 PSO2**



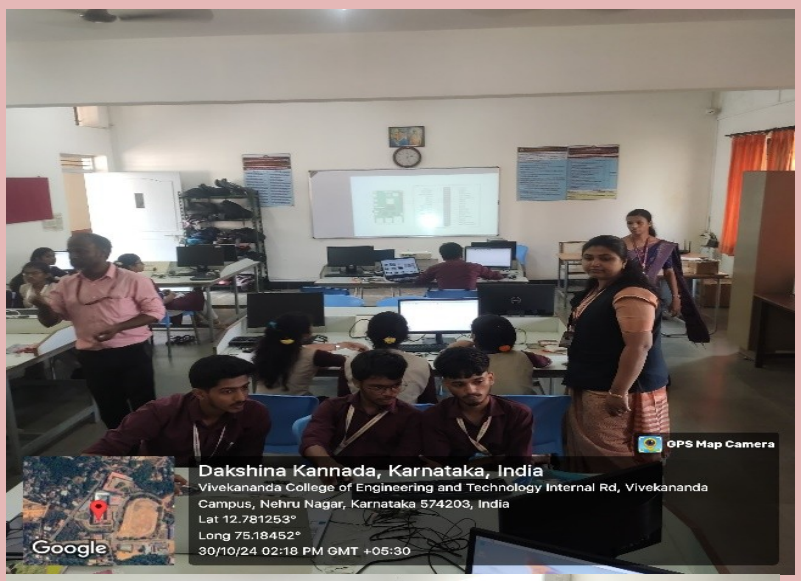


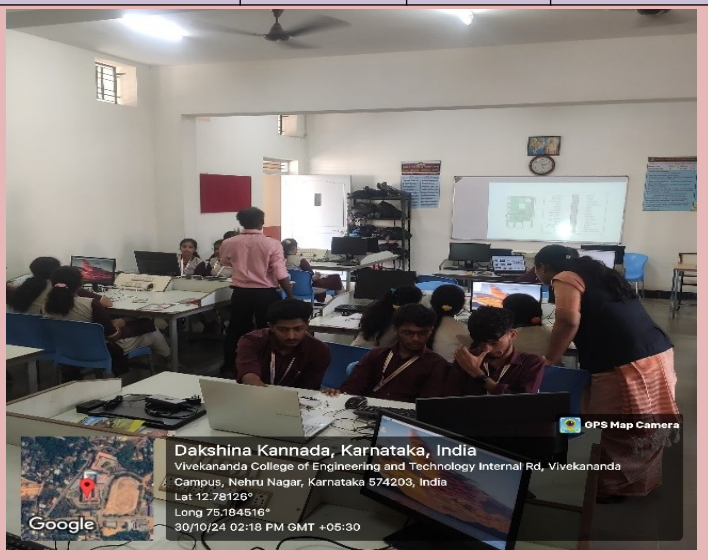
The Department of Electronics and Communication Engineering at **Vivekananda College of Engineering & Technology, Puttur**, organized a **technical talk** on **"Preparing for the SASKEN Placement drive"** on 10**th October 2024**. The session was conducted by **Mr. Akshay Kumar,**Software Engineer Kernel & Driver Linux Sasken, Bangalore.

During the talk, Mr. Akshay Kumar provided valuable insights into the **SASKEN recruitment process**, emphasizing the various stages involved, the skills and knowledge required to excel, and the preparation strategies that can help candidates stand out. He focused on areas such as **technical assessments**, **coding rounds**, and **interviews**, shedding light on what recruiters look for in potential candidates.

The session was attended by a final year students, who were primarily from the final year, providing them with an excellent opportunity to gain insights directly from an industry expert. It was a highly informative session, helping the students prepare for their upcoming recruitment drive with confidence and clarity.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
4	A workshop on "Arduino Uno and its applications"	30.10.2024	Prof. Nisha G R Prof. Shreyas H Prof. Shivaprasad and Prof. Mahabaleshwar Bhat P	3rd	63	PO1 PO2 PO5









Department of Electronics and Communication Engineering, Vivekananda College of Engineering and Technology, Puttur in coordination with Association of Electronics and Communication Engineering and ICIoT LAB, VCET organized a workshop on "Arduino Uno and its applications"

on 30/10/2024 in ICIoT lab, VCET Puttur. Prof. Nisha G R, Prof. Shreyas H, Prof. Shivaprasad and Prof. Mahabaleshwar Bhat P have conducted the workshop. Around 63 students of 2nd year ECE got benefited from this workshop. Students were divided into batches and training was given to them in batches so that they could focus on hands on experience. The workshop mainly focused on real world applications of IoT with Arduino Uno microcontroller. Students have learnt various experiments like LED glowing with switch, working of IR sensors etc.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
5	A talk on **Placement Guidance.**	04-11-2024	**Ms. Jesmitha Menezes** Alumna, ECE Batch 2020 Module lead, Happiest Mind Technologies Pvt. Ltd.	7th	64	PO1 PO2 PO7 PO8 PO12 PSO2





The **Department of Electronics and Communication Engineering at Vivekananda College of Engineering & Technology, Puttur,** successfully organized a technical talk on **"Placement Guidance"** on 4th November 2024. The session was conducted by **Ms. Jesmitha Menezes, a Module Lead at Happiest Minds Technologies Pvt. Ltd.,** who delivered an insightful and engaging talk aimed at preparing students for their upcoming placement opportunities.

During her session, **Ms. Jesmitha Menezes** provided **valuable insights** into the **campus recruitment process**, including the various stages involved, such as **aptitude tests, technical**

interviews, group discussions, and HR interviews. She shared **practical tips and strategies** on how students can enhance their technical skills, build a strong resume, and effectively communicate their knowledge and abilities during interviews.

She also emphasized the **importance of soft skills**, professional networking, and staying updated with the latest industry trends to stand out in a competitive job market. By sharing her own experiences and industry expectations, she provided students with a **real-world perspective** on what recruiters look for in potential candidates.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
6	Food fest "VCET Bhojanam"	07-11-2024	Coordinated by Prof. Nithin ECE Dept, VCET, Puttur	3rd	63	PO9 PO10 PO12



On **7th November 2024,** the Department of Electronics & Communication Engineering organized a food fest, "**VCET Bhojanam,**" for second-year students, where six groups of ten students each prepared dishes with medicinal properties, under the coordination of **Prof. Nithin**. The event provided an opportunity for students to explore the health benefits of various ingredients, promoting both teamwork and an appreciation for nutritious, medicinal foods. The fest not only highlighted the students' culinary skills but also emphasized the importance of traditional healing ingredients in everyday diets. Through "VCET Bhojanam," students gained insights into the medicinal values of foods, fostering a deeper understanding of nutrition and wellness. The event was a unique blend of learning and enjoyment, creating a memorable experience for everyone involved.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
7	**Product Launch**	12-11-2024				





The initiative "Lakshya," originating from the Department of Electronics and Communication Engineering (ECE), has successfully designed and implemented innovative products, including:

Automatic Controller for Soft Drink Filling Machine:

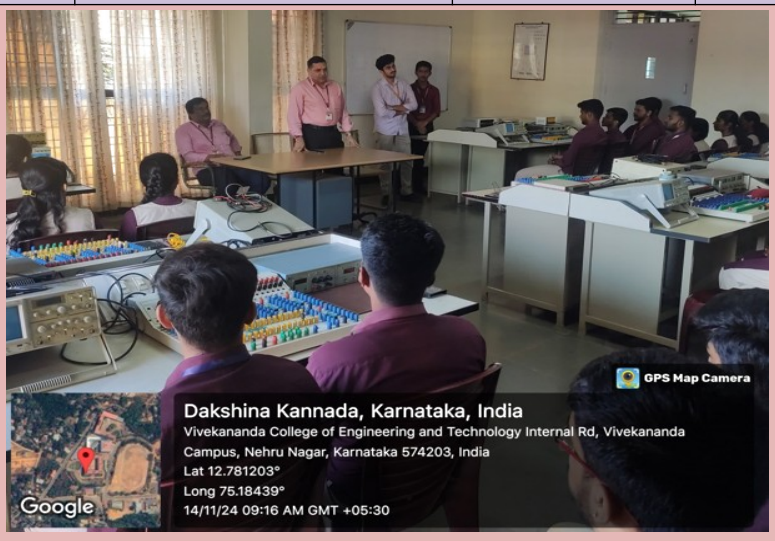
1. **Purpose**: Automates the soft drink filling process, ensuring precision and efficiency.
2. **Installation**: Installed at **Mahalaxmi Beverages**, located near the railway station in Puttur.

Power Indicator:

1. **Purpose**: Monitors and indicates power status effectively.
2. **Installation**: Installed at **Ananthadi**.

These achievements highlight the technological innovation and practical problem-solving focus of the Lakshya initiative.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
8	The **Circuit Decoding competition,** hosted by the Association of **Electronics and Communication Engineering (AECE) in collaboration with CoRE at VCET, Puttur.**	14-11-2024	**Akshay S P**	**All Year** **ECE** **Students**	150	**PO1,PO2** **PO3,PO4** **PO5,PO6** **PO8,PO9** **PO10,PO11** **PSO1**





The **Circuit Decoding Competition**, organized by the **Association of Electronics and Communication Engineering (AECE)** in collaboration with **CoRE**, was successfully held at **Vivekananda College of Engineering & Technology (VCET), Puttur**, on **14th November 2024**. This competition was open to **all ECE students across different academic years**, providing them with an excellent platform to **test and enhance their circuit analysis and problem-solving skills**. The event aimed to challenge participants with **intricate circuit-based puzzles**, requiring them to apply their **theoretical knowledge and practical understanding** to decode complex circuit configurations within a given time frame.

Students demonstrated their **technical expertise, logical reasoning, and teamwork** as they tackled the problem statements. The competition not only fostered **a spirit of innovation and analytical thinking** but also encouraged students to **hone their real-time debugging and troubleshooting skills**, which are crucial for careers in electronics and

embedded systems.

The event witnessed **enthusiastic participation** from students of all years, making it a highly engaging and intellectually stimulating competition. It successfully promoted **collaborative learning and technical excellence**, reinforcing the significance of **practical knowledge** in the field of Electronics and Communication Engineering.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
9	A motivational talk on **"FOCUS"**	21-11-2024	Sri Sundara Gaura Das & Sri LokamangalKrishna Das Yoth Mentor ISKCON, Mangaluru.	3rd	63	PO8 PO12





On **21st November 2024**, a **motivational talk** on **"FOCUS"** was organized at **Vivekananda College of Engineering & Technology, Puttur**. The session was conducted by **Sri Sundara Gaura Das** and **Sri Lokamangal Krishna Das**, who are **youth mentors from ISKCON, Mangaluru**.

The talk aimed to inspire students by highlighting the **importance of focus, discipline, and mindfulness** in achieving academic and personal success. The speakers shared **valuable insights and practical techniques** on how to develop a **focused mindset**, overcome distractions, and cultivate a sense of purpose in life. They emphasized the **power of concentration, goal-setting, and self-discipline**, encouraging students to stay determined in their pursuits.

A total of **63 students (3rd)** attended the session, actively participating and expressing their appreciation for the **insightful guidance and wisdom shared**.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
10	Simulation of circuits using **"Multi-sim"**	21-11-2024 & 27-11-2024	Mrs Sowmya Anil and Mrs Rajani Rai B EC Dept. VCET, Puttur.	3rd	63	**PO1 PO2 PO5 PSO1**





The **Department of Electronics and Communication Engineering** at **Vivekananda College of Engineering & Technology (VCET), Puttur**, organized a **hands-on workshop** on **"Simulation of Circuits using MultiSim"** on **21st and 27th November 2024**. The session was conducted by **Mrs. Sowmya Anil** and **Mrs. Rajani Rai B**, who guided the students through the **fundamentals and advanced concepts** of circuit simulation using **NI MultiSim**.

The workshop was specifically designed for **third-year students**, with **63 participants** actively engaging in the sessions. The objective was to provide students with **practical exposure** to circuit design, analysis, and troubleshooting using MultiSim, a widely used electronic circuit simulation tool.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
11	Research article writing methods and usage of similarity checking tools	30-11-2024	Dr. Shrikanth Rao S.K HOD, Dept. ECE VCET, Puttur.	5th	60	PO5 PO7 PO11





The **Department of Electronics and Communication Engineering** at **Vivekananda College of Engineering & Technology (VCET), Puttur)** organized an **informative session** on **"Research Article Writing Methods and Usage of Similarity Checking Tools"** on **30th November 2024**. The session was conducted by **Dr. Shrikanth Rao S.K, Head of the Department (HOD), ECE, VCET, Puttur**.

This session was specifically designed for **fifth-semester students**, with **60 participants** actively engaging in the workshop. The objective was to equip students with **essential skills in research writing**, ensuring **academic integrity** and **effective scholarly communication**.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
12	A workshop on **"Simulation of Digital Electronic Circuits using PSpice"**	09-12-2024	Dr. Mahanthesh R Choudhary & Prof. Shreyas H, EC Dept. VCET, Puttur.	7th	60	PO1,PO2 PO3,PO4 PO5 PO11 PSO1





The **Department of Electronics and Communication Engineering** at **Vivekananda College of Engineering & Technology (VCET), Puttur**, organized a **hands-on workshop** on **"Simulation of Digital Electronic Circuits using PSpice"** on **9th December 2024**. The session was conducted by **Dr. Mahanthesh R. Choudhary** and **Prof. Shreyas H** from the **ECE Department, VCET, Puttur**.

The workshop was specifically designed for **third-year students**, with **60 participants** actively engaging in the sessions. The objective was to introduce students to **PSpice**, a widely used circuit simulation software, and provide **practical exposure to digital circuit design and analysis**.

The workshop was **highly interactive and engaging**, allowing students to gain **hands-on experience** in **circuit simulation, debugging techniques, and digital system verification**. The event successfully helped students **enhance their technical proficiency**, preparing them for **future academic research, project development, and industrial applications**.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
13	Simulation of different digital modulation techniques using **"Simulink"**.	10-12-2024	Prof. Mahabaleshwara Bhat P Prof. Prabha G S Prof. Nithin EC Dept. VCET, Putuur.	5th	56	PO1,PO2 PO3,PO4 PO5 PO11 PSO2





The **Department of Electronics and Communication Engineering** at **Vivekananda College of Engineering & Technology (VCET), Puttur)** organized a **hands-on workshop** on **"Simulation of Different Digital Modulation Techniques using Simulink"** on **10th December 2024**. The session was conducted by **Prof. Mahabaleshwara Bhat P, Prof. Prabha G S, and Prof. Nithin** from the **ECE Department, VCET, Puttur**.

This workshop was specifically designed for **fifth-semester students**, with **56 participants** actively engaging in the sessions. The objective was to provide students with **practical exposure to digital modulation techniques**, their implementation, and analysis using **MATLAB Simulink**.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
14	Hands-on session on **"Introduction to Electronic Circuits".**	01-01-2025	Prof. Nisha G R Prof. Nirupama K EC Dept. VCET, Putuur.	1st CD	52	PO1 PO2 PO5 PSO1





The **Department of Electronics and Communication Engineering** at **Vivekananda College of Engineering & Technology (VCET), Puttur)** organized a **hands-on session** on **"Introduction to Electronic Circuits"** on **1st January 2025**. The session was conducted by **Prof. Nisha G. R.** and **Prof. Nirupama K** from the **ECE Department, VCET, Puttur**.

This session was specifically designed for **first-year CD students**, with **52 participants** actively engaging in the workshop. The objective was to introduce students to **the fundamentals of electronic circuits**, helping them build a strong foundation in **circuit theory, component identification, and practical circuit implementation**.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
15	**Prestigious Outstanding Student Volunteer award at** IEEE Mangalore Subsection and IEEE Bangalore Section	11-01-2025	-------	--------	--------	----





Chandraprakash Shankar Naik, a final-year Electronics and Communication student at **VCET, Puttur**, has been honored with the **PRESTIGIOUS OUTSTANDING STUDENT VOLUNTEER AWARD** by the **IEEE Mangalore Subsection and IEEE Bangalore Section.** This recognition is a testament to his remarkable contributions, dedication, and leadership in promoting **IEEE's** mission and initiatives.

Chandraprakash has played a pivotal role in organizing various technical events, fostering student engagement, and contributing to the growth of the **IEEE** community. His efforts have significantly enhanced the visibility of **IEEE** at his institution and beyond, inspiring fellow students to actively participate in professional and technical development opportunities. This award not only acknowledges his hard work but also highlights his commitment to driving innovation and collaboration within the **IEEE** ecosystem.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No. s	
16	A session on **"Arduino Uno and its applications"**	17-01-2025	Prof. Akshay S P & Prof . Nithin EC Dept. VCET, Puttur	1st CS-A	55	PO1,PO2 PO3,PO4 PO5,PO6 PO8,PO10 PO11 PSO1





The Department of Electronics and Communication Engineering at Vivekananda College of Engineering & Technology (VCET), Puttur) organized a hands-on workshop on "**Arduino Uno and its applications**" on 17th January 2025. The session was conducted by Prof. Akshay S P and Prof. Nithin from the ECE Department, VCET, Puttur.

During the workshop, participants were introduced to the fundamental concepts of Arduino, including its architecture, programming, and interfacing with various sensors and modules. The instructors guided students through real-time projects, helping them understand how Arduino can be used in automation, robotics, IoT applications, and embedded systems.

The workshop provided a strong foundation for students to explore embedded systems and microcontroller programming, fostering their interest in electronics and innovation. The event was well-received, with active participation and enthusiasm from students of **1st CS-A**, making it a valuable learning experience.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
17	A Workshop on Electronic Circuit Simulation Using **"PSpice and Laksya Software. "**	17-01-2025	Prof. Prabha G EC Dept. VCET, Puttur	1st CS-B	56	**PO1** PO2 PO5 PSO1





The Department of Electronics and Communication Engineering at Vivekananda College of Engineering & Technology (VCET), Puttur, is organizing a hands-on workshop on **'Electronic Circuit Simulation Using PSpice and Laksya Software'** on **17th January 2025**. The session will be conducted by **Prof. Prabha G S** from the **EC Department, VCET, Puttur**, exclusively for **1st CS-B students**. This workshop aims to provide practical insights into circuit simulation techniques, helping students enhance their understanding of electronic circuit design and analysis.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
18	A session on **"Arduino Uno and its applications"**	20-01-2025	Prof. Akshay S P & Prof . Nithin EC Dept. VCET, Puttur	1st EC	55	**PO1**,PO2 PO3,PO4 PO5,PO6 PO8,PO10 PO11 PSO1





The **Department of Electronics and Communication Engineering** at **Vivekananda College of Engineering & Technology (VCET), Puttur,** is pleased to organize a **hands-on session on "Arduino Uno and its Applications"** for the **1st-year EC students** on **20th January 2025**.

This interactive session will be conducted by **Prof. Akshay S P** and **Prof. Nithin** from the **EC Department, VCET, Puttur**, aiming to provide students with a **fundamental understanding of Arduino Uno, its programming, interfacing with sensors, and real-time applications in embedded systems and automation.**

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
19	A Workshop on Electronic Circuit Simulation Using **"PSpice and Laksya Software."**	21-01-2025	Prof. Prabha G S & Prof. Nithin EC Dept. VCET, Puttur	1st CS-A	63	**PO1** PO2 PO5 PSO1





The **Department of Electronics and Communication Engineering** at **Vivekananda College of Engineering & Technology (VCET), Puttur,** is organizing a **hands-on workshop on "Electronic Circuit Simulation Using PSpice and Laksya Software"** for **1st-year CS-A students** on **21st January 2025**.

This workshop, conducted by **Prof. Prabha G S** and **Prof. Nithin** from the **EC Department, VCET, Puttur**, aims to provide students with **practical exposure to circuit design, analysis, and simulation using industry-standard software tools like PSpice and Laksya.**

This workshop will **bridge the gap between theoretical concepts and practical implementation,** enabling students to develop skills essential for **circuit analysis, embedded systems, and electronics design.**

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
20	A Workshop on Electronic Circuit Simulation Using **"PSpice and Laksya Software."**	22-01-2025	Prof. Rajani Rai B Prof. PrabhaG.S. Prof. Sowmya Anil EC Dept.VCET Puttur.	1st EC	55	PO1,PO2 PO3,PO4 PO5,PO11 PSO1





The **Department of Electronics and Communication Engineering** at **Vivekananda College of Engineering & Technology (VCET), Puttur**, is organizing a hands-on workshop on **"Electronic Circuit Simulation Using PSpice and Laksya Software"** for **1st-year EC students** on **22nd January 2025**.

The session will be conducted by **Prof. Rajani Rai B**, **Prof. Prabha G. S.**, and **Prof. Sowmya Anil**, all from the **EC Department, VCET, Puttur**. The workshop aims to provide students with practical experience in circuit design, analysis, and simulation using industry-standard software tools. Participants will learn the basics of circuit simulation, gain hands-on experience with **PSpice** for troubleshooting and analyzing circuits, and explore the features of **Laksya Software** for simulating both analog and digital circuits. The workshop will include practical exercises, offering students an opportunity to work on real-world applications and mini-projects.

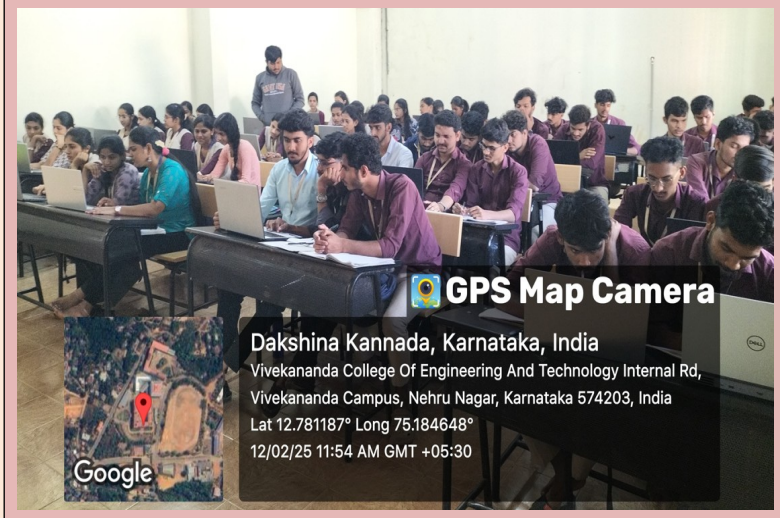
ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
21	**A Placement Training on C-programming and Data Structure.**	17-02-2025 to 23-02-2025	Pavan Ethnus, Bangalore.	4th	62	PO8 PO9 PO10 PO11 PO12

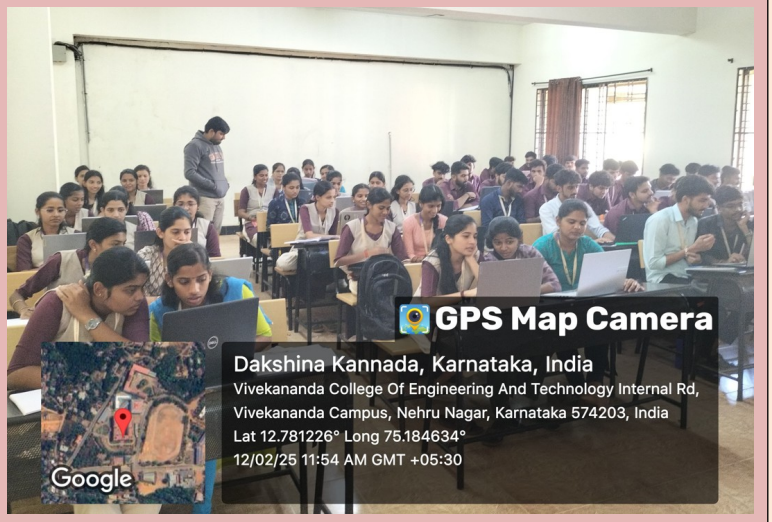




A **Placement Training on C-Programming and Data Structures** will be conducted by **Pavan from Ethnus, Bangalore**, from **17th February 2025 to 23rd February 2025**. This training program is designed to strengthen students' understanding of ***C* programming concepts and data structures**, focusing on problem-solving techniques essential for technical interviews and competitive coding. The sessions will include **hands-on coding exercises, algorithm optimization strategies, and real-world problem-solving approaches**, equipping students with the skills required to excel in placement tests and job interviews.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
22	A Placement Training on C++.	07-02-2025 to 22-02-2025	Sankhajit Banerjee Q-Spider, Bangalore	6th	64	PO8 PO9 PO10 PO11 PO12





A **Placement Training on C++** will be conducted by **Sankhajit Banerjee from Q-Spider, Bangalore**, from **7th February 2025 to 22nd February 2025** for **6th-semester ECE students**. This training aims to enhance students' proficiency in **C++ programming**, covering **object-oriented concepts, data structures, and algorithmic problem-solving techniques** essential for technical interviews and placement exams. The sessions will include **hands-on coding exercises, real-world applications, and interview-focused problem-solving**, preparing students for a successful career in the tech industry.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
23	Faculty industrial visit to **KarMic Design Pvt. Ltd. Manipal**	21-02-2025	Rathnakar Bhat CEO, Director Gajanan Shanbhag CFO, Director Dilipa Mogaveera & Gururaj Bankapur	---	----	------





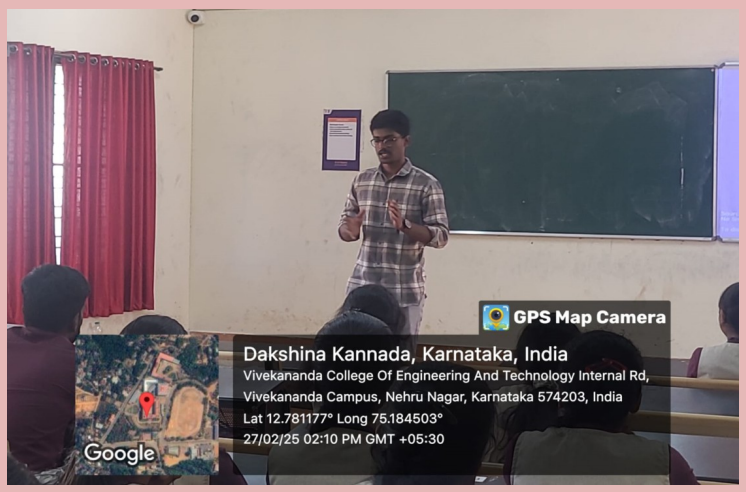




A faculty industrial visit to KarMic Design Pvt. Ltd., Manipal, was organized today. It was a wonderful experience for all the faculty members of the department. An interaction with industry experts was scheduled, during which faculty members discussed how practical concepts of semiconductors can be effectively incorporated into the curriculum to make students industry-ready. The hospitality extended by the KarMic team was truly exceptional.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
24	A talk on **Placement Guidance**.	27-02-2025	Sujith and Rahul EC, Dept.	6th	64	PO1 PO2 PO7 PO8 PO12 PSO2





The Department of Electronics and Communication Engineering at Vivekananda College of Engineering & Technology (VCET), Puttur, is organizing a talk on "Placement Guidance" for 6th Sem EC students on 27th February 2025.

The session will be conducted by **Rahul** and **Sujith**, from the **EC Department, VCET, Puttur**. This session aims to provide valuable insights into **career opportunities, interview preparation, resume building, and essential skills for successful placements**, helping students navigate the recruitment process with confidence. Total 64 students are participated.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
25	A talk on **Placement Guidance.**	27-02-2025	Ojaswee EC, Dept. Ananya CS , Dept	6th	64	PO1 PO2 PO7 PO8 PO12 PSO2





The Department of Electronics and Communication Engineering at Vivekananda College of Engineering & Technology (VCET), Puttur, is organizing a talk on "Placement Guidance" for 6th Sem EC students on 27th February 2025.

The session will be conducted by **Ananya** from **CS** Department and **Ojaswee** from the **EC Department, VCET, Puttur**. This session aims to provide valuable insights into **career opportunities, interview preparation, resume building, and essential skills for successful placements**, helping students navigate the recruitment process with confidence. Total 64 students are participated.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
26	A talk on **Placement Guidance.**	11-03-2025	Aheesha Shabaraya Divya Harshithashree M Varna S	4th & 6th	126	PO1 PO2 PO7 PO8 PO12 PSO2





The Department of Electronics and Communication Engineering at Vivekananda College of Engineering & Technology (VCET), Puttur, is organizing a talk on "Placement Guidance" for 4th and 6th Sem EC students on 11th March 2025.

The session will be conducted by Aheesha Shabaraya Divya Harshithashree M and Varna S from the **EC Department, VCET, Puttur**. This session aims to provide valuable insights into **career opportunities, interview preparation, resume building, and essential skills for successful placements**, helping students navigate the recruitment process with confidence. Total 126 students are participated.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
27	Technical Online Lecture Serieson **"GLOBAL CHALLENGES AND OPPORTUNITIES IN THE GEN-AI ERA".**	17-03-2025	Conducted by VTU, Belgaum.	6th	55	PO1 PO2 PO5 PO11 PSO2





The **Technical Online Lecture Series** on **"Global Challenges and Opportunities in the Gen-AI Era"** is scheduled to take place on **17th March 2025** and is being conducted by **Visvesvaraya Technological University (VTU), Belgaum**. This lecture series aims to provide a comprehensive understanding of the rapidly evolving field of **Generative Artificial Intelligence (Gen-AI)** and its far-reaching impact on industries, education, and society at large.

The session will delve into the **global challenges** posed by the integration of Gen-AI, such as ethical concerns, data privacy, and workforce displacement, while also highlighting the **opportunities** it presents in terms of innovation, automation, and problem-solving across diverse sectors. This event is expected to offer valuable insights for students, researchers, and professionals, equipping them with the knowledge and perspective needed to thrive in this new era of technological advancement. Total 55 students are participated.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
28	A three-day Workshop on **Network Simulation and Protocol Performance using NS2.**	24-04-2025 to 26-04-2025	**Dr. Pavitra** **Dr. Swapnil** Associate Professor Department of ECE, Dayananda Sagar College of Engineering, Bangalore.	4th& 6th	50	PO1,PO2 PO3,PO4 PO5,PO8 PO11 PSO2





The Department of Electronics and Communication Engineering at Vivekananda College of Engineering & Technology, Puttur, organized a three-day workshop on **"Network Simulation and Protocol Performance using NS2"** from 24th April to 26th April 2025.
The workshop was conducted by **Dr. Pavitra** and **Dr. Swapnil,** Associate Professors from the Department of ECE, Dayananda Sagar College of Engineering, Bangalore.

The hands-on sessions aimed to equip students and faculty with practical knowledge of network simulation techniques, protocol analysis, and performance evaluation using the **NS2** simulator. The workshop served as a valuable platform for enhancing technical skills and understanding the real-time application of network protocols. Total 50 students are participated.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
29	A talk on implementation of verilog code on Digilent Basys-3 Artix-7 FPGA board.	07-05-2025	Aheesha Shabaraya Final Year EC Student.	6th	64	PO1 PO2 PO7 PO8 PO12 PSO2





The Department of Electronics and Communication Engineering at Vivekananda College of Engineering & Technology (VCET), Puttur, organized a talk on **"Implementation of Verilog Code on Digilent Basys-3 Artix-7 FPGA Board"** on 7th May 2025.
The session was delivered by **Ms. Aheesha Shabaraya**, a Final Year EC student of VCET, Puttur.

The talk focused on the practical aspects of **Verilog HDL** programming, along with step-by-step guidance on implementing the code on the **Basys-3 Artix-7 FPGA** development board. It provided a great opportunity for students to gain insights into FPGA-based digital design, bridging the gap between theory and real-world hardware implementation. Total 64 students are participated.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
30	A PCB design workshop by **ALTIUM**.	22-05-2025	Shipra Srivastava Marketing Program Manager-Altium Bengaluru.	6th	50	PO1,PO2 PO3,PO4 PO5,PO6 PO11 PSO1





The Department of Electronics and Communication Engineering at Vivekananda College of Engineering & Technology (VCET), Puttur, organized a PCB Design Workshop by **ALTIUM** on 22nd May 2025. The workshop was conducted by **Ms. Shipra Srivastava**, Marketing Program Manager at Altium, Bengaluru.

The session focused on introducing participants to professional PCB design practices using Altium tools, covering key concepts such as schematic capture, layout design, and signal integrity. The workshop provided valuable industry insights and hands-on exposure to PCB design software, enhancing the technical skill set of the students.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
31	**Simulation and Layout Design of Digital Circuits Using DSCH2 and Microwind software.**	27-05-2025	Ms. Rajani Rai B and Ms. Sowmya Anil ECE Dept. VCET, Puttur.	6th	50	PO1, PO2 PO3, PO4 PO5,PO11 PSO1





The Department of Electronics and Communication Engineering at Vivekananda College of Engineering & Technology, Puttur organized a workshop on "**Simulation and Layout Design of Digital Circuits Using DSCH2 and Microwind software**" on 27th May 2025. The session was conducted by Ms. Rajani Rai B and Ms. Sowmya Anil from the ECE Department, VCET, Puttur.

On 27th May 2025, a practical session on VLSI circuit design and simulation was conducted for the 6th semester Electronics and Communication Engineering students. The activity included both schematic design and layout generation, followed by Design rule checks (DRC) and transient analysis using the simulation tools provided in Microwind. The simulation results (voltage vs. time waveform) were analyzed to validate the functionality of the designed circuits. The activity effectively bridged theoretical concepts with practical VLSI design using DSCH2 and Microwind.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
32	**Comparative Analysis of 1-bit SRAM Cells using 90nm and 180nm CMOS Technologies.**	27-05-2025	Disha and Navya Final Year EC Students.	6th	64	PO1 PO2 PO7 PO8 PO12 PSO2





The Department of Electronics and Communication Engineering at Vivekananda College of Engineering & Technology, Puttur, organized a talk on "**Comparative Analysis of 1-bit SRAM Cells using 90nm and 180nm CMOS Technologies**" for 6th Sem EC students on 27th May 2025.

The session was conducted by Disha and Navya from the Final year **EC Department, VCET, Puttur**. The session aimed to explore how scaling down CMOS technology impacts memory cell performance, focusing on 6T SRAM cells. The session provided a hands-on perspective on CMOS technology scaling through a comparative study of 1-bit SRAM cells at 90nm and 180nm nodes. The presenters effectively showcased the trade-offs between power efficiency and stability in nanoscale designs, offering practical insights into SRAM design challenges. The activity encouraged peer learning, technical discussions, and a deeper understanding of VLSI concepts among participants.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
33	A Workshop on **"Basic Electric Circuits and .Motors"**.	23-06-2025	Prof. Prabha G S and Prof. Nithin EC Dept. VCET Puttur.	2nd AI	51	**PO1 PO2 PO3 PO12 PSO1**



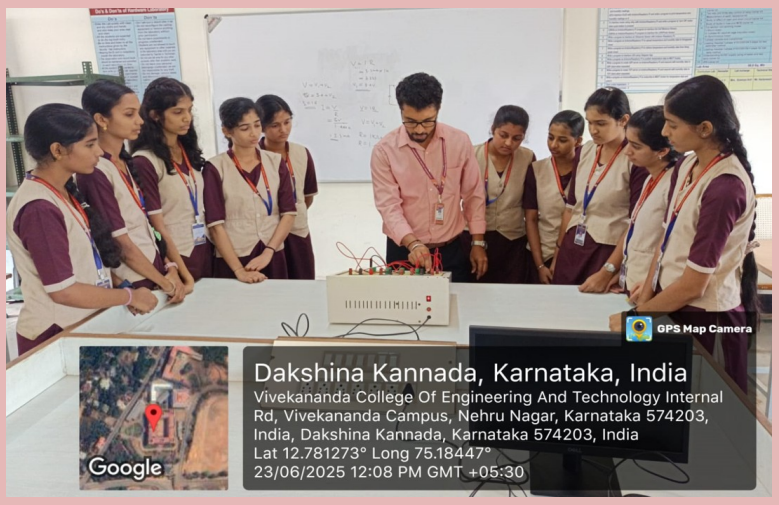


The Department of Electronics and Communication Engineering at Vivekananda College of Engineering & Technology (**VCET**), Puttur, conducted an insightful workshop on "***Basic Electric Circuits and Motors***" for second-semester Artificial Intelligence students on June 23, 2025.

Led by **Prof. Prabha G. S.** and **Prof. Nithin** from the EC Department, the session offered hands-on demonstrations and clear explanations of core electrical concepts. Topics included Kirchhoff's Voltage and Current Laws (KVL, KCL), three-phase AC and the operating principles of both DC and AC motors.

The workshop drew active participation from 51 students, giving them a strong practical foundation in circuit theory and motor applications.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
34	A Workshop on **"Basic Electric Circuits and .Motors"**.	23-06-2025	Prof. Prabha G S and Prof. Nithin	2nd CD	52	**PO1 PO2 PO3 PO12 PSO1**





The Department of Electronics and Communication Engineering at Vivekananda College of Engineering & Technology (**VCET**), Puttur, conducted an insightful workshop on "***Basic Electric Circuits and Motors***" for second-semester Data Science students on June 23, 2025.

Led by **Prof. Prabha G. S.** and **Prof. Nithin** from the EC Department, the session offered hands-on demonstrations and clear explanations of core electrical concepts. Topics included Kirchhoff's Voltage and Current Laws (KVL, KCL), three-phase AC and the operating principles of both DC and AC motors.

The workshop drew active participation from 52 students, giving them a strong practical foundation in circuit theory and motor applications.

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
35	A hands-on session on **"Arduino Uno and its Applications"**	24-06-2025	Prof. Akshay S P and Prof. Nithin EC Dept. VCET, Puttur.	2nd AI	51	**PO1 PO2 PO3 PO12 PSO1**





The **Department of Electronics and Communication Engineering** at **Vivekananda College of Engineering & Technology (VCET), Puttur,** is pleased to organize a **hands-on session on "Arduino Uno and its Applications"** for the second**-year AI students** on **24th June 2025**.

This interactive session will be conducted by **Prof. Akshay S P** and **Prof. Nithin** from the **EC Department, VCET, Puttur**, aiming to provide students with a **fundamental understanding of Arduino Uno, its programming, interfacing with sensors, and real-time applications in embedded systems and automation. A total of 51 second-year AI students actively participated in the session.**

ACADEMIC YEAR 2024-25						
SNo	Activity Details	DD.MM.YYYY	Details of Resource Person(s)/ Guest	Details of Participants		POs & PSOs
				Semester	No.s	
36	A hands-on session on **"Arduino Uno and its Applications"**	24-06-2025	Prof. Akshay S P and Prof. Nithin EC Dept. VCET, Puttur.	2nd AI	51	**PO1 PO2 PO3 PO12 PSO1**





The **Department of Electronics and Communication Engineering** at **Vivekananda College of Engineering & Technology (VCET), Puttur,** is pleased to organize a **hands-on session on "Arduino Uno and its Applications"** for the second**-year AI students** on **24th June 2025**.

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ACADEMIC YEAR 2024-25						
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The **Department of Electronics and Communication Engineering** at **Vivekananda College of Engineering & Technology (VCET), Puttur,** is pleased to organize a **hands-on session on "Arduino Uno and its Applications"** for the second**-year CD students** on **24th June 2025**.

This interactive session will be conducted by **Prof. Akshay S P** and **Prof. Nithin** from the **EC Department, VCET, Puttur**, aiming to provide students with a **fundamental understanding of Arduino Uno, its programming, interfacing with sensors, and real-time applications in embedded systems and automation.**