



The technical online lecture series on “Global Challenges and Opportunities in the Gen-AI Era” was scheduled on 17<sup>th</sup> March 2025 and conducted by Visvesvaraya Technological University, Belgaum. This lecture series provided a comprehensive understanding of the rapidly evolving field of Generative Artificial Intelligence (Gen-AI) and its far-reaching impact on industries, education, and society at large. The session delved into the global challenges posed by the integration of Gen-AI, such as ethical concerns, data privacy, and workforce displacement, while also highlighting the opportunities it presents in terms of innovation, automation, and problem-solving across diverse sectors. This event gave valuable insights for students, researchers, and professionals, equipping them with the knowledge and perspective needed to thrive in this new era of technological advancement. Total 64 students participated in the event.

# CONGRATULATIONS!

Electronics & Communication Engineering students placed during the Campus Recruitment 2024-25



**G A AHEESHA SHABARAYA**  
CADENCE



**JEEVITH S**  
SAP LABS



**DIVYA**  
IWAVE



**HARSHITHASHREE M**  
IWAVE



**APEKSHA K**  
GINGER MEDIA



**KARTHIK K**  
BREVERA TECHNOLOGIES



**OJASWEE**  
KARMIC



**RAHUL K**  
KARMIC



**SUJITH**  
KARMIC



**SUSHMITHA K**  
KARMIC



**M KAVAN**  
RAVICAB



**SEEMANTH KUMAR K**  
RAVICAB



**SHRINIVAS K**  
PLUGINHIVE



**VARNA**  
TVS ELECTRONICS



**LAXMINARASIMHA**  
LOGYCENT



**LIKHITHA K**  
LOGYCENT



**SHRAVAN KUMAR RAI**  
LOGYCENT



**SRUJAN**  
LIGHT MECHANICS



**TILAK D**  
LIGHT MECHANICS



**SHRIHARI V RAO**  
SOLAREEDGE



**SHREESHA KUMAR**  
V3 NOVAS



**SATHWIK V NAYAK**  
THYSSENKRUPP



**ASHWATH P S GOWDA**  
DESICREW SOLUTIONS



**SUJEESH**  
DESICREW SOLUTIONS



**GANESHA KUMARA K**  
PARAS WIRES



**CHANDRAPRAKASH S**  
ADVANCED RAIL



### VISION

To produce comprehensively trained, innovative, technically competent, creative and socially responsible engineers contributing to nation's imprint on the world stage.

### MISSION

- M1: Establish a unique academic environment to enable the students to face the challenges of the electronics and communication engineering field.
- M2: To impart quality education on par with industry requirements through state of art infrastructure, highly qualified and competent faculty.
- M3: Build cutting edge technology hub adding to understudies making progress
- M4: To collaborate association with government, industry, society, alumni to promote entrepreneurship
- M5: Develop student centric methodology by creative teaching-learning environment

Chief Editor  
**Dr. Shrikanth Rao S. K.**

Editor  
**Mrs. Nisha G.R.**

Associate Editor  
**Mr. Rajesh A.**

Student Editor  
**Ms. Harshithashree**

### TWO RANKS IN THE DEPARTMENT

## Congratulations!

Mr. Rahul K. and Ms. Likhitha K. of Electronics and Communication Engineering branch has secured 7<sup>th</sup> and 9<sup>th</sup> rank in Visveswaraya Technology University, Belagavi for the academic year 2024-25.



### STAFF ACTIVITY

#### FACULTY INDUSTRIAL VISIT TO KARMIC DESIGN PVT. LTD., MANIPAL



A faculty industrial visit to KarMic Design Pvt. Ltd., Manipal, was organized on 21<sup>st</sup> february 2025. It was a wonderful experience for all the faculty members of the department. An interaction with industry experts was scheduled, during which faculty members discussed how practical concepts of semiconductors can be effectively incorporated into the curriculum to make students industry ready. The hospitality extended by the KarMic team was truly exceptional.

### STAFF ACHIEVEMENTS



The orientation day at Yenepoya Institute of Technology, Moodabidri was held on 4<sup>th</sup> April 2025, with the primary objective of introducing students to the institute's academic environment and the professional growth opportunities available through IEEE. The session featured Mrs. Sowmya Anil, Assistant Professor at VCET, Puttur, as the esteemed Resource Person. She shared valuable insights into the role of IEEE in shaping students' technical and professional journeys. Accompanying her was Mr. Jeevith S, who shared his personal experiences and journey as an active IEEE member.



Prof. Rajani Rai B, Assistant Professor, Department of Electronics and communication Engineering, VCET, Puttur has conducted an activity on “How to plan for start-up-Engineering the future of healthcare: The power of biomedical signals” at Dayananda Sagar College of Engineering, Bangalore on 6<sup>th</sup> May 2025 at 2 PM.

Prof. Rajani Rai B. published a research article titled “Advancements in Epilepsy Classification: Current Trends and Future Directions” in MethodsX journal, 2025.

### SPORTS ACHIEVEMENTS

Ms. Rashmi A. of final year ECE , Ms. Rachana K. D. and Ms. Shravya L. K. of 2<sup>nd</sup> year ECE participated in VTU state level Kabaddi women tournament 2025-26, which was held at Kalpatharu Institute of Technology, Thiptur from 16<sup>th</sup> to 18<sup>th</sup> May 2025.

Mr. Y. P. Diganth Mandanna of 2<sup>nd</sup> year ECE participated in state level Hockey tournament 2025-26, which was held at Ballari Institute of Technology & Management, Ballari from 15<sup>th</sup> to 17<sup>th</sup> May 2025.

Mr. Gurucharan & Mr. Shravan of final year ECE participated in Mangaluru division Kabaddi tournament 2025-26, which was held at P. A. College of Engineering, Mangaluru on 12<sup>th</sup> April 2025.



## STUDENT ACHIEVEMENTS FOR THE ACADEMIC YEAR 2024-25

Varna S, S. Roopa and Likitha k of final year EC attended 5<sup>th</sup> National Conference on next generation intelligence in Electronics and Communication Engineering on 5<sup>th</sup> May 2025 and presented a paper titled **"Braille Bridge: An innovative system to bridge the gap between blind persons"** held at East point College of Engineering and Technology, Bangalore and also recognised as the **Best Paper (GOLD)** in NCIEC 2025.

Jeevith S. and Sathvika V. Nayak of final year EC have participated and exhibited the project titled **"Automatic classification of fetal heart rate using machine learning"** in I2connect-2025 project competition hosted on Mangalore Institute of Technology and Engineering, Moodabidri on 19<sup>th</sup> April 2025.

## STUDENT ACTIVITIES



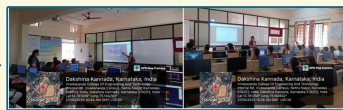
The Department of Electronics and Communication Engineering at Vivekananda College of Engineering & Technology, Puttur, organized a three day workshop on **"Network Simulation and Protocol Performance using NS2"** from 24<sup>th</sup> April to 26<sup>th</sup> April 2025. The workshop was conducted by Dr. Pavitra and Dr. Swapnil, Associate Professor from the Department of ECE, Dayananda Sagar College of Engineering, Bangalore. The hands-on sessions aimed to equip students and faculty with practical knowledge of network simulation techniques, protocol analysis, and performance evaluation using the NS2 simulator. The workshop served as a valuable platform for enhancing technical skills and understanding the real-time application of network protocols. Total 64 students participated.

The Department of Electronics and Communication Engineering at Vivekananda College of Engineering & Technology, Puttur, organized a talk on **"Implementation of Verilog Code on Digilent Basys-3 Artix-7 FPGA Board"** on 7<sup>th</sup> May 2025. The session was delivered by Mr. Aheesha Shabaraya, final year EC student of VCET, Puttur. The talk focused on the practical aspects of Verilog HDL programming, along with step-by-step guidance on implementing the code on the Basys-3 Artix-7 FPGA development board. It provided a great opportunity for students to gain insights into FPGA-based digital design, bridging the gap between theory and real-world hardware implementation. Total 64 students participated.



The Department of Electronics and Communication Engineering at Vivekananda College of Engineering & Technology, Puttur, organized a PCB Design Workshop by **ALTIUM** on 22<sup>nd</sup> May 2025. The workshop was conducted by Ms. Shipra Srivastava, Marketing Program Manager at Altium, Bengaluru. The session focused on introducing participants to professional PCB design practices using Altium tools, covering key concepts such as schematic capture, layout design, and signal integrity. The workshop provided valuable industry insights and hands-on exposure to PCB design software, enhancing the technical skill set of the students.

The Department of Electronics and Communication Engineering at Vivekananda College of Engineering & Technology, Puttur organized a workshop on **"Simulation and Layout Design of Digital Circuits Using DSCH2 and Microwind software"** on 27<sup>th</sup> May 2025. The session was conducted by Ms. Rajani Rai B. and Ms. Sowmya Anil from the ECE Department, VCET, Puttur. On 27<sup>th</sup> May 2025, a practical session on VLSI circuit design and simulation was conducted for the 6<sup>th</sup> semester Electronics and Communication Engineering students. The activity included both schematic design and layout generation, followed by Design rule checks (DRC) and transient analysis using the simulation tools provided in Microwind. The simulation results (voltage vs. time waveform) were analyzed to validate the functionality of the designed circuits. The activity effectively bridged theoretical concepts with practical VLSI design using DSCH2 and Microwind.



The Department of Electronics and Communication Engineering at Vivekananda College of Engineering & Technology, Puttur, is organized a talk on **"Comparative Analysis of 1-bit SRAM Cells using 90nm and 180nm CMOS Technologies."** for 6<sup>th</sup> Sem EC students on 27<sup>th</sup> May 2025. The session was conducted by Disha and Navya from the Final year EC Department, VCET, Puttur. The session aimed to explore how scaling down CMOS technology impacts memory cell performance, focusing on 6T SRAM cells. The session provided a hands-on perspective on CMOS technology scaling through a comparative study of 1-bit SRAM cells at 90nm and 180nm nodes. The presenters effectively showcased the trade-offs between power efficiency and stability in nanoscale designs, offering practical insights into SRAM design challenges. The activity encouraged peer learning, technical discussions, and a deeper understanding of VLSI concepts among participants.

A placement training on **C-Programming and Data Structures** was conducted by Pavan from Ethnus, Bangalore, from 17<sup>th</sup> February 2025 to 23<sup>rd</sup> February 2025. This training program was designed to strengthen students' understanding of C programming concepts and data structures, focusing on problem-solving techniques essential for technical interviews and competitive coding. The sessions included hands-on coding exercises, algorithm optimization strategies, and real-world problem solving approaches, equipping students with the skills required to excel in placement tests and job interviews.



A placement training on C++ was conducted by Sankhajt Banerjee from Q-Spider, Bangalore, from 7<sup>th</sup> February 2025 to 22<sup>nd</sup> February 2025 for 6<sup>th</sup>-semester ECE students. This training aimed to enhance students' proficiency in C++ programming, covering object-oriented concepts, data structures, and algorithmic problem-solving techniques essential for technical interviews and placement exams. The sessions included hands-on coding exercises, real-world applications, and interview-focused problem solving, preparing students for a successful career in the tech industry.

The Department of Electronics and Communication Engineering at Vivekananda College of Engineering & Technology, Puttur, organized a talk on **"Placement Guidance"** for 6<sup>th</sup> Sem EC students on 27<sup>th</sup> February 2025. The session was conducted by Rahul and Sujith, from the EC Department, VCET, Puttur. This session provided valuable insights into career opportunities, interview preparation, resume building, and essential skills for successful placements, helping students navigate the recruitment process with confidence. Total 64 students participated.



The Department of Electronics and Communication Engineering at Vivekananda College of Engineering & Technology, Puttur, is organized a talk on **"Placement Guidance"** for 6<sup>th</sup> Sem EC students on 27<sup>th</sup> February 2025. The session was conducted by Ms. Ananya from CS Department and Mr. Ojaswee from the EC Department, VCET, Puttur. This session provided valuable insights into career opportunities, interview preparation, resume building, and essential skills for successful placements, helping students navigate the recruitment process with confidence. Total 64 students participated in the activity.

The Department of Electronics and Communication Engineering at Vivekananda College of Engineering & Technology, Puttur, organized a talk on **"Placement Guidance"** for 4<sup>th</sup> and 6<sup>th</sup> Sem EC students on 11<sup>th</sup> March 2025. The session was conducted by Mr. Aheesha Shabaraya, Ms. Divya, Ms. Harshithashree M. and Ms. Varna S. from the EC Department, VCET, Puttur. This session provided valuable insights into career opportunities, interview preparation, resume building, and essential skills for successful placements, helping students navigate the recruitment process with confidence. Total 126 students participated.

