



Vivekananda College of Engineering & Technology

[Sponsored by Vivekananda Vidyavardhaka Sangha, Puttur ®]

Affiliated to Visvesvaraya Technological University

Approved by AICTE New Delhi & Govt of Karnataka

TRP05
Rev 1.1
CSE
21/12/2016

TIMETABLE

Dept: CSE		Sem: IV		Div: A		Room No.: A232		Branch: CSE		Version: I		W.E.F: 27/01/2017	
Time		I 9:00-9:55	II 9:55-10:50	III 11:10-12:05	IV 12:05-1:00	V 2:00-2:55	VI 2:55-3:50	VII 3:50-4:45	VIII *				
Day													
Day 1	Th	M&M (MP)	MAT (SA)	DAA (HN)	OOO (RA)	SE (SP)	DC+ (NK)	DAA+ (HN)	-				
	Pr								-				
Day 2	Th	M&M (MP)	DC (NK)	MAT (SA)	DAA (HN)	DAA Lab (A1&A2: HN +TPS) M&M Lab (A3&A4: MP +SKP)			-				
	Pr								-				
Day 3	Th	SE (SP)	M&M (MP)	OOO (RA)	MAT (SA)	DAA Lab (A3&A4: HN +TPS) M&M Lab (A1&A2: MP +SKP)			-				
	Pr								-				
Day 4	Th	VIKAS	DAA (HN)	SE (SP)	DC (NK)	MAT+ (SA)	M&M+ (MP)	TUT	-				
	Pr	(TH)							-				
Day 5	Th	MAT (SA)	M&M (MP)	OOO (RA)	DC (NK)	OOO+ (RA)	SE+ (SP)	TUT	-				
	Pr								-				
Day 6	Th	DAA (HN)	SE (SP)	DC (NK)	OOO (RA)	<---Forum Activity (SKR+SM+HN)--->			-				
	Pr								-				
Faculty Details				Subject Details:				* Extra / + Tutorial Classes					
SA		Ms. Smitha		MAT	15MAT 41	Engineering Mathematics - IV							
SP		Ms. Shaila Patil		SE	15CS 42	Software Engineering							
HN		Mr. Harivinod N		DAA	15CS 43	Design and Analysis of Algorithms							
MP		Mr. Mahesh Prasanna K		M&M	15CS 44	Microprocessors and Microcontrollers							
RA		Ms. Rakshitha		OOO	15CS 45	Object Oriented Concepts							
NK		Mr. Nagaraj K		DC	15CS 46	Data Communication							
HN + TPS		Mr. Harivinod N + Ms. Thapaswini PS		DAA LAB	15CSL 47	Design and Analysis of Algorithm Laboratory							
MP + SKP		Mr. Mahesh Prasanna K + Mr. Sandesha Karanth PK		M&M LAB	15CSL 48	Microprocessor and Microcontroller Laboratory							
Batch Details:		A1: 01 - 16	A2: 17 - 33	A3: 34 - 49	A4: 50 - 66	Class Adviser: SKR + SM + HN							

Prepared by: Mahesh Prasanna K

Checked by: Radhika Shetty DS

HOD: Mahesh Prasanna K

Nehru Nagar, Puttur - 574 203, DK, Karnataka State - INDIA.

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TRP05
Rev 1.1
CSE
21/12/2016

TIMETABLE

Dept: CSE		Sem: IV		Div: B		Room No.: A235		Branch: CSE		Version: I		W.E.F: 27/01/2017	
Time		I 9:00-9:55	II 9:55-10:50	III 11:10-12:05	IV 12:05-1:00	V 2:00-2:55	VI 2:55-3:50	VII 3:50-4:45	VIII *				
Day													
Day 1	Th	SE (SP)	M&M (SKP)	DC (RKK)	DAA (TPS)	MAT+ (RK)	DAA+ (TPS)	M&M+ (SKP)	-				
	Pr								-				
Day 2	Th	MAT (RK)	M&M (SKP)	OOO (SM)	SE (SP)	OOO (SM)	DC+ (RKK)	TUT	-				
	Pr								-				
Day 3	Th	VIKAS	DAA (TPS)	M&M (SKP)	SE (SP)	OOO+ (SM)	SE+ (SP)	TUT	-				
	Pr	(RA)							-				
Day 4	Th	DAA (TPS)	MAT (RK)	OOO (SM)	DC (RKK)	DAA Lab (B1&B2: TPS+SM) M&M Lab (B3&B4: NUB+SKP)			-				
	Pr								-				
Day 5	Th	SE (SP)	DC (RKK)	MAT (RK)	DAA (TPS)	DAA Lab (B3&B4: TPS+SM) M&M Lab (B1&B2: NUB+SKP)			-				
	Pr								-				
Day 6	Th	OOO (SM)	MAT (RK)	DC (RKK)	M&M (SKP)	<---Forum Activity (RKK+RTK)--->			-				
	Pr								-				
Faculty Details				Subject Details:				* Extra / + Tutorial Classes					
RK		Mr. Ramananda Kamath		MAT	15MAT 41	Engineering Mathematics - IV							
SP		Ms. Shaila Patil		SE	15CS 42	Software Engineering							
TPS		Ms. Thapaswini PS		DAA	15CS 43	Design and Analysis of Algorithms							
SKP		Mr. Sandesha Karanth PK		M&M	15CS 44	Microprocessors and Microcontrollers							
SM		Ms. Savitha M		OOO	15CS 45	Object Oriented Concepts							
RKK		Mr. Raghavendra KK		DC	15CS 46	Data Communication							
TPS + SM		Ms. Thapaswini PS + Ms. Savitha M		DAA LAB	15CSL 47	Design and Analysis of Algorithm Laboratory							
NUB + SKP		Mr. Nagesh UB + Mr. Sandesha Karanth PK		M&M LAB	15CSL 48	Microprocessor and Microcontroller Laboratory							
Batch Details:		B1: 01 - 18	B2: 19 - 36	B3: 37 - 54	B4: 55 - 71	Class Adviser:		RKK + RTK					

Prepared by: Mahesh Prasanna K

Checked by: Radhika Shetty DS

HOD: Mahesh Prasanna K

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TRP05
Rev 1.1
CSE
21/12/2016

TIMETABLE

Dept: CSE		Sem: VI		Div: A		Room No.: A202		Branch: CSE		Version: I		W.E.F: 27/01/2017	
Time		I 9:00-9:55	II 9:55-10:50	III 11:10-12:05	IV 12:05-1:00	V 2:00-2:55	VI 2:55-3:50	VII 3:50-4:45	VIII *				
Day		CG (SMR)	USP (BK)	CN2 (NUG)	CD (RGK)	USP+ (BK)	CD+ (RGK)	CG+ (SMR)	-				
Day 1	Th	CG (SMR)	USP (BK)	CN2 (NUG)	CD (RGK)	USP+ (BK)	CD+ (RGK)	CG+ (SMR)	-				
	Pr								-				
Day 2	Th	VIKAS	OR (HN)	M&E (NKH)	CN2 (NUG)	CG Lab (A1&A2: BK+RA) USPCD Lab (A3&A4: SPS+NKH)			-				
	Pr	(SP)							-				
Day 3	Th	OR (HN)	CD (RGK)	USP (BK)	CG (SMR)	CG Lab (A3&A4: BK+RA) USPCD Lab (A1&A2: SPS+NKH)			-				
	Pr								-				
Day 4	Th	USP (BK)	CN2 (NUG)	CG (SMR)	OR (HN)	M&E (NKH)	CN2+ (NUG)	TUT	-				
	Pr								-				
Day 5	Th	CN2 (NUG)	M&E (NKH)	CD (RGK)	USP (BK)	OR+ (HN)	M&E+ (NKH)	TUT	-				
	Pr								-				
Day 6	Th	CD (RGK)	CG (SMR)	OR (HN)	M&E (NKH)	<---Forum Activity (SMR+NUG+NKH)--->			-				
	Pr								-				
Faculty Details				Subject Details:								* Extra / + Tutorial Classes	
NKH		Mr. NischayKumar Hegde		M&E	10AL 61	Management and Entrepreneurship							
BK		Ms. Bharathi K		USP	10CS 62	Unix System Programming							
RGK		Ms. Roopa GK		CD	10CS 63	Compiler Design							
NUG		Mr. Nithin Kurup UG		CN2	10CS 64	Computer Networks – II							
SMR		Ms. Santhosh Meharwade		CG	10CS 65	Computer Graphics and Visualization							
HN		Mr. Harivinod N		OR	10CS 661	Operations Research							
BK + RA		Ms. Bharathi K + Mr. Rakshitha		CG LAB	10CSL 67	Computer Graphics and Visualization Laboratory							
SPS + NKH		Ms. Sharanya PS + Mr. NischayKumar Hegde		USPCD LAB	10CSL 68	Unix System Programming and Compiler Design Laboratory							
Batch Details:		A1: 01 – 16	A2: 17 – 33	A3: 34 – 49	A4: 50 – 66	Class Adviser:		SMR + NUG + NKH					

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Rev 1.1
CSE
21/12/2016

TIMETABLE

Dept: CSE		Sem: VI		Div: B		Room No.: A203		Branch: CSE		Version: I		W.E.F: 27/01/2017	
Time		I 9:00-9:55	II 9:55-10:50	III 11:10-12:05	IV 12:05-1:00	V 2:00-2:55	VI 2:55-3:50	VII 3:50-4:45	VIII *				
Day		9:00-9:55	9:55-10:50	11:10-12:05	12:05-1:00	2:00-2:55	2:55-3:50	3:50-4:45	4:45-5:15				
Day 1	Th	VIKAS	CN2 (NUB)	CG (SMR)	USP (SPS)	M&E (RTK)	OR+ (SKR)	USP+ (SPS)	-				
	Pr	(SPS)							-				
Day 2	Th	M&E (RTK)	CD (RGK)	USP (SPS)	CG (SMR)	CN2+ (NUB)	CG+ (SMR)	TUT	-				
	Pr								-				
Day 3	Th	CG (SMR)	M&E (RTK)	CN2 (NUB)	OR (SKR)	CD+ (RGK)	M&E+ (RTK)	TUT	-				
	Pr								-				
Day 4	Th	OR (SKR)	USP (SPS)	CD (RGK)	CN2 (NUB)	CG Lab (B1&B2: SMR+BK) USPCD Lab (B3&B4: RGK+SPS)			-				
	Pr								-				
Day 5	Th	CD (RGK)	OR (SKR)	CG (SMR)	M&E (RTK)	CG Lab (B3&B4: SMR+BK) USPCD Lab (B1&B2: RGK+SPS)			-				
	Pr								-				
Day 6	Th	USP (SPS)	CN2 (NUB)	OR (SKR)	CD (RGK)				-				
	Pr					<---Forum Activity (NK+SP)--->			-				
Faculty Details				Subject Details:				* Extra / + Tutorial Classes					
RTK		Mr. Raghavendra TK		M&E	10AL 61	Management and Entrepreneurship							
SPS		Ms. Sharanya PS		USP	10CS 62	Unix System Programming							
RGK		Ms. Roopa GK		CD	10CS 63	Compiler Design							
NUB		Mr. Nagesh UB		CN2	10CS 64	Computer Networks - II							
SMR		Mr. Santhosh Meharwade		CG	10CS 65	Computer Graphics and Visualization							
SKR		Mr. Sharath KR		OR	10CS 661	Operations Research							
SMR + BK		Mr. Santhosh Meharwade + Ms. Bharathi K		CG LAB	10CSL 67	Computer Graphics and Visualization Laboratory							
RGK + SPS		Ms. Roopa GK + Ms. Sharanya PS		USPCD LAB	10CSL 68	Unix System Programming and Compiler Design Laboratory							
Batch Details:		B1: 01 - 10	B2: 11 - 20	B3: 21 - 30	B4: 31 - 39	Class Adviser: NK + SP							

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CSE
21/12/2016

TIMETABLE

Dept: CSE		Sem: VIII		Div: A		Room No.: A209		Branch: CSE		Version: I		W.E.F: 27/01/2017	
Time		I 9:00-9:55	II 9:55-10:50	III 11:10-12:05	IV 12:05-1:00	V 2:00-2:55	VI 2:55-3:50	VII 3:50-4:45	VIII *				
Day		SA (RTK)	SMS (RDS)	INS (NKH)	ST (SKR)	INS+ (NKH)	Seminar (SKR+NK)						
Day 1	Th												
	Pr												
Day 2	Th	INS (NKH)	ST (SKR)	SA (RTK)	SMS (RDS)	SA+ (RTK)	Seminar (SKR+NK)						
	Pr												
Day 3	Th	ST (SKR)	INS (NKH)	SMS (RDS)	SA (RTK)	SMS+ (RDS)	Seminar (SKR+NK)						
	Pr												
Day 4	Th	SMS (RDS)	SA (RTK)	ST (SKR)	INS (NKH)	ST+ (SKR)	Seminar (SKR+NK)						
	Pr												
Day 5	Th	-	-	-	-	-	-	-	-	-	-	-	-
	Pr	Project Work (BK+PKP)						Project Work (BK+PKP)					
Day 6	Th	-	-										
	Pr	Project Work (BK+PKP)						<---Forum Activity (RDS+SPS+RGK)--->					
Faculty Details				Subject Details:				* Extra / + Tutorial Classes					
RTK		Mr. Raghavendra TK		SA	10IS 81	Software Architectures							
RDS		Ms. Radhika Shetty DS		SMS	10CS 82	System Modeling and Simulation							
NKH		Mr. NischayKumar Hegde		INS	10CS 835	Information and Network Security							
SKR		Mr. Sharath KR		ST	10CS 842	Software Testing							
BK + PKP		Ms. Bharathi K + Mr. PramodKumar PM		PW	10CS 85	Project Work							
SKR		Mr. Sharath KR + Mr. Nagaraj K		Seminar	10CS 86	Seminar							
-		-	-	-	-	Class Adviser: RDS + SPS + RGK							

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21/12/2016

TIMETABLE

Dept: CSE		Sem: VIII		Div: B		Room No.: A210		Branch: CSE		Version: I		W.E.F: 27/01/2017	
Time		I 9:00-9:55	II 9:55-10:50	III 11:10-12:05	IV 12:05-1:00	V 2:00-2:55	VI 2:55-3:50	VII 3:50-4:45	VIII *				
Day		SA (TH)	SMS (UK)	INS (SYM)	ST (NK)	SMS+ (UK)	Seminar (SKR+NK)						
Day 1		Th											
		Pr											
Day 2		Th	SMS (UK)	SA (TH)	ST (NK)	INS (SYM)	ST+ (NK)	Seminar (SKR+NK)					
		Pr											
Day 3		Th	INS (SYM)	SMS (UK)	ST (NK)	SA (TH)	INS+ (SYM)	Seminar (SKR+NK)					
		Pr											
Day 4		Th	ST (NK)	SMS (UK)	SA (TH)	INS (SYM)	SA+ (TH)	Seminar (SKR+NK)					
		Pr											
Day 5		Th	-	-	-	-	-	-	-				
		Pr	Project Work (BK+PKP)				Project Work (BK+PKP)						
Day 6		Th	-	-									
		Pr	Project Work (BK+PKP)				<---Forum Activity (RK+BK)--->						
Faculty Details				Subject Details:								* Extra / + Tutorial Classes	
TH		Ms.Thanmayee		SA	10IS 81		Software Architectures						
UK		Ms. Usha Kiran		SMS	10CS 82		System Modeling and Simulation						
SYM		Ms. Saumya YM		INS	10CS 835		Information and Network Security						
NK		Mr. Nagaraj K		ST	10CS 842		Software Testing						
BK + PKP		Ms. Bharathi K + Mr. PramodKumar PM		PW	10CS 85		Project Work						
SKR		Mr. Sharath KR + Mr. Nagaraj K		Seminar	10CS 86		Seminar						
-		-	-	-	-		Class Adviser: RK + BK						

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